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Pure Storage FlashArray-Implementation-Specialist Exam Syllabus Topics:

Topic	Details
Topic 1	• Pre-Installation • Upgrade: This section of the exam measures the skills of Enterprise Infrastructure Technicians and covers all preparation activities before deploying or upgrading a Pure Storage FlashArray. It includes understanding environmental requirements, verifying prerequisites, checking compatibility, and validating system readiness through appropriate tools and documentation.
Topic 2	• Upgrades: This section of the exam measures the skills of FlashArray Implementation Specialists and focuses on tasks involved in managing firmware and software upgrades. Candidates must demonstrate knowledge of upgrade planning, verification steps, and rollback procedures, ensuring that systems are updated with minimal disruption to service.

Topic 3	• Post-Installation • Upgrade: This section of the exam measures the skills of FlashArray Implementation Specialists and evaluates how professionals confirm system functionality after installation or an upgrade. It involves validating connectivity, running health checks, confirming configurations, and ensuring that the deployment meets operational expectations.
Topic 4	• Installation: This section of the exam measures the skills of Enterprise Infrastructure Technicians and focuses on executing a successful installation of FlashArray systems. It tests the ability to perform physical setup, cabling, configuration of network settings, and the application of initial system configurations necessary for full deployment.

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Pure Storage Certified FlashArray Implementation Specialist Sample Questions (Q57-Q62):

NEW QUESTION # 57
Which ports are used by default for replication on a FlashArray//XR4?

- A. ETH2 and ETH3
- B. ETH3 and ETH4
- C. ETH0 and ETH1

Answer: A

Explanation:
The FlashArray//XR4 maintains the standard port assignment convention established in previous generations for its onboard Ethernet interfaces.
ETH2 and ETH3 are the default ports designated for Replication traffic.
ETH0 and ETH1 are reserved for Management.
ETH2 and ETH3 are configured by Purity defaults to handle the heavy bandwidth of asynchronous or synchronous replication.
While these assignments can be modified in software, the physical ports labeled eth2 and eth3 on the rear of the chassis are the intended primary interfaces for this function. Implementation Engineers should cable these ports to the replication network switches during the initial install.
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NEW QUESTION # 58
Which enablement feature will cause Mirrored Write statistics to appear in the Analysis section of the FlashArray Purity 6.x GUI?

- A. ActiveCluster (AC)
- B. FA File
- C. ActiveDR

Answer: A

Explanation:
Mirrored Write statistics appear in the Purity GUI's Analysis section when ActiveCluster is enabled, as it tracks synchronous replication activity between clusters.

NEW QUESTION # 59
Which power cable receptacle type does the FlashArray//X accessory kit contain?

- A. C14 to C15
- B. C19 to C20
- C. C13 to C14

Answer: C

Explanation:
Standard enterprise data center power distribution units (PDUs) typically use C13 or C19 outlets. The FlashArray//X series power supplies utilize C14 inlets (the male connector on the chassis side). Therefore, the standard accessory kit included with a FlashArray//X ships with C13 to C14 power cables.
The C13 connector plugs into the array's power supply (matching the C14 inlet), and the C14 connector on the other end is designed to plug into a standard rack PDU (which typically has C13 outlets). This is the industry-standard "jumper cord" for rack-mounted server and storage hardware.
It is important for the Implementation Engineer to verify this during the pre-install phase. If the customer's facility uses non-standard PDUs (like those with C19 outlets or wall-style plugs), the standard kit cables will not fit, and the engineer must ensure the correct cables were ordered separately or provided by the customer. However, for the exam and standard BOM validation, the correct answer is the ubiquitous C13 to C14 cable type.
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NEW QUESTION # 60
After running puredb run giveback --safe command once CT0 has been upgraded, how long should the Implementation Engineer wait before continuing with the controller upgrade?

- A. 10 minutes
- B. 5 minutes
- C. 3 minutes

Answer: A

Explanation:
After executing the puredb run giveback --safe command during a manual controller upgrade or specific non-disruptive upgrade (NDU) procedure, the Implementation Engineer must wait for 10 minutes before proceeding to the next step (typically upgrading the peer controller).
The giveback command instructs the recently upgraded controller (e.g., CT0) to resume its primary role and take back ownership of its assigned storage resources and I/O handling from the peer controller. This process triggers a failover event for the host multipathing software.
Path Stabilization: The 10-minute wait time is a critical safety buffer mandated to ensure that all host-side multipathing drivers (MPIO) have successfully recognized the restored paths to the upgraded controller and have stabilized.
Risk Mitigation: Proceeding too quickly-before the hosts have fully settled and paths are marked as "Active/Optimized"-could lead to an "all paths down" (APD) scenario when the second controller is taken offline for its upgrade. This wait time verifies that the upgraded node is fully functional and carrying load, adhering to the strict "availability first" philosophy of FlashArray upgrades.

NEW QUESTION # 61
FlashArray//C and //E models use which flash storage architecture, identifiable by gray tabs on the DirectFlash Module carriers?

- A. SLC Flash
- B. TLC Flash
- C. QLC flash

Answer: C

Explanation:
Pure Storage differentiates its product lines based on the type of NAND flash used, optimizing for either performance or capacity/cost.
FlashArray//X uses TLC (Triple-Level Cell) flash for high performance and endurance. These modules typically have orange tabs.

FlashArray//C and FlashArray//E are designed for high-capacity, capacity-optimized workloads. They utilize QLC (Quad-Level Cell) flash.
QLC flash stores 4 bits per cell, offering higher density at a lower cost per terabyte, but with different endurance characteristics managed by the DirectFlash software. To help engineers and customers physically distinguish these modules, QLC DirectFlash Modules feature gray release tabs on the carrier. Identifying these tabs confirms that the correct media type is being installed into the capacity-oriented //C or //E chassis.

NEW QUESTION # 62
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