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The NVIDIA NCP-AII practice test software also keeps a record of attempts, keeping users informed about their progress and allowing them to improve themselves. This feature makes it easy for NCP-AII desktop-based practice exam software users to focus on their mistakes and overcome them before the original attempt. Overall, the Windows-based NVIDIA AI Infrastructure (NCP-AII) practice test software has a user-friendly interface that facilitates candidates to prepare for the NVIDIA NCP-AII exam without facing technical issues.

NVIDIA AI Infrastructure Sample Questions (Q86-Q91):

NEW QUESTION # 86
Your AI training pipeline involves a pre-processing step that reads data from a large HDF5 file. You notice significant delays during this step. You suspect the HDF5 file structure might be contributing to the slow read times. What optimization technique is MOST likely to improve read performance from this HDF5 file?

- A. Converting the HDF5 file to a CSV file.
- B. Encrypting the HDF5 file for enhanced security.
- C. Compressing the HDF5 file using gzip.
- D. Reorganizing the HDF5 file to improve data contiguity and chunking.
- E. Storing the HDF5 file on a network file system like NFS.

Answer: D

Explanation:
Reorganizing the HDF5 file (option C) to improve data contiguity and chunking is the most effective optimization. HDF5 performance is highly dependent on how the data is laid out within the file. Contiguous data and optimal chunk sizes allow for more efficient 1/0 operations. Converting to CSV (A) loses the hierarchical structure of HDF5. Storing on NFS (B) adds network overhead. Compression (D) can reduce storage space but increases decompression overhead. Encryption (E) adds overhead without improving read performance.

NEW QUESTION # 87
You're deploying a distributed training workload across multiple NVIDIAAIOO GPUs connected with NVLink and InfiniBand. What steps are necessary to validate the end-to-end network performance between the GPUs before running the actual training job? (Select all that apply)

- A. Employ 'iperf3' or 'nc' to measure TCP/UDP bandwidth between nodes over the InfiniBand network.
- B. Use 'ibstat' to verify the status and link speed of the InfiniBand interfaces on each node.
- C. Ping all nodes to confirm basic network connectivity
- D. Manually inspect the physical cabling of NVLink bridges and InfiniBand connections.
- E. Run NCCL tests (e.g., to measure NVLink bandwidth and latency between GPUs on the same node.

Answer: A,B,E

Explanation:
Validating end-to-end network performance requires checking NVLink performance within nodes (NCCL tests), verifying InfiniBand interface status Cibstat), and measuring inter-node bandwidth using tools like 'iperf3' or 'ne. Pinging is a basic connectivity check, but doesn't assess bandwidth. Physical cabling is important to visually inspect, however ibstat also shows the active link and cabling errors.

NEW QUESTION # 88
A customer reports that after installing an additional NVIDIA GPU into their existing AI server, they are experiencing IOMMU related errors during VM startup that leverage GPU pass-through. The error message indicates a problem with address translation. Which troubleshooting steps should be taken? (Select TWO)

- A. Ensure that IOMMIJ is enabled in the BIOS/UEFI, and that the kernel command line includes (for Intel CPUs) or (for AMD CPUs).
- B. Reduce the amount of RAM allocated to the VM to avoid exceeding addressable memory limits.
- C. Reinstall the NVIDIA drivers within the VM.
- D. Disable IOMMIJ in the BIOS/UEFI. This resolves the address translation conflicts.
- E. Update the motherboard BIOS/UEFI to the latest version, as newer versions often include improved IOMMIJ support and bug fixes.

Answer: A,E

Explanation:
IOMMU errors often stem from incorrect configuration or outdated firmware. Disabling IOMMU is generally not recommended as it reduces security. Ensuring IOMMIJ is enabled in the BIOS/UEFI and kernel command line is crucial. Updating the BIOS/IJEFI

addresses potential bugs in IOMMU implementation. While memory allocation could be a factor, it is not the most likely immediate cause. Reinstalling drivers within the VM is less likely to resolve IOMMU-related errors.

NEW QUESTION # 89

After deploying BlueField OS, you notice that the network interfaces are not automatically configured with IP addresses. Which of the following actions would be the MOST appropriate first step to troubleshoot this issue?

- A. Restart the networking service using 'systemctl restart networking'.
- B. Check the DHCP client configuration to ensure it is enabled and properly configured to request IP addresses. Examine the logs for any errors.
- C. Re-flash the Bluefield OS image
- D. Reinstall the Mellanox OFED drivers. A corrupted driver installation could cause network configuration issues.
- E. Manually assign static IP addresses to the interfaces using the 'ifconfig' command.

Answer: B

Explanation:
In most modern systems, network interfaces are automatically configured using DHCP. Therefore, the first step is to check if the DHCP client is enabled and configured correctly. If DHCP fails, then other troubleshooting steps, such as static IP assignment or driver reinstallation, can be considered.

NEW QUESTION # 90

You are deploying a new A1 inference service using Triton Inference Server on a multi-GPU system. After deploying the models, you observe that only one GPU is being utilized, even though the models are configured to use multiple GPUs. What could be the possible causes for this?

- A. The GPUs are not of the same type and Triton cannot properly schedule across them.
- B. Insufficient CPU cores are available for the Triton Inference Server, limiting its ability to spawn multiple inference processes.
- C. The models are not optimized for multi-GPU inference, resulting in a single GPU bottleneck.
- D. The model configuration file does not specify the 'instance_group' parameter correctly to utilize multiple GPUs.
- E. The Triton Inference Server is not configured to enable CUDA Multi-Process Service (MPS).

Answer: D,E

Explanation:
The 'instance_group' parameter in the model configuration dictates how Triton distributes the model across GPUs. Without proper configuration, it may default to a single GPIJ. CUDA MPS allows multiple CUDA applications (in this case, Triton inference processes) to share a single GPU, improving utilization. Insufficient CPU cores or non-optimized models could limit performance, but wouldn't necessarily restrict usage to a single GPIJ. While dissimilar GPIJs can affect performance, Triton will attempt to schedule across them if configured correctly.

NEW QUESTION # 91

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