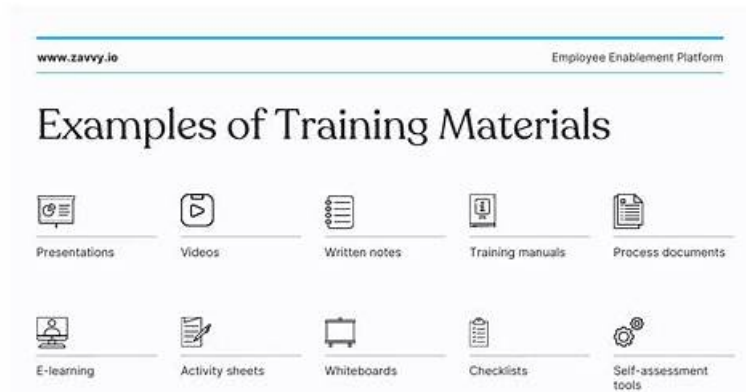


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NEW QUESTION # 75

The rate of SF64 and SL64 boards is STM54. Therefore, the two plates can be interconnected.

- A. False
- B. True

Answer: A

NEW QUESTION # 76

The interval between two adjacent pointer alignment events is at least () frames.

- A. 0

- B. 1
- C. 2
- D. 3

Answer: C

NEW QUESTION # 77

In the Colored & Directed scenario of 4D ROADM, how many sets of M40V+D40 are needed to dispatch local services to four directions?

- A. 0
- B. 1
- C. 2
- D. 3

Answer: C

NEW QUESTION # 78

Which of the following IP addresses is a Class C IP address?

- A. 10011010.01101110.10000000.01101111
- B. C2.A8.D2.e1
- C. 10.110.192.111
- D. 127.0.0.1

Answer: B

NEW QUESTION # 79

Which of the following descriptions about the process of multiplexing ODU1 to ODU2 is correct?

- A. ODU1->ODTU12->OPU2->ODTUG2->ODU2
- B. ODU1->ODTUG2->ODTU12->OPU2->ODU2
- C. ODU1->OPU2->ODTU12->ODTUG2->ODU2
- D. ODU1->ODTU12->ODTUG2->OPU2->ODU2

Answer: D

NEW QUESTION # 80

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