

2025 Free H31-341_V2.5 Practice Exams - Huawei HCIP-Transmission V2.5 - The Best Latest H31-341_V2.5 Exam Questions Vce



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Huawei H31-341_V2.5 exam covers a wide range of topics related to the planning, design, implementation, and management of transmission networks. Some of the key topics covered in the exam include network topology design, network capacity planning, network security, and network optimization. To pass the exam, candidates must demonstrate a solid understanding of these topics and be able to apply their knowledge to real-world scenarios.

Huawei H31-341_V2.5 (HCIP-Transmission V2.5) exam is a certification exam designed to validate the skills and knowledge of professionals who work with Huawei transmission technology. It covers a wide range of topics related to the design, implementation, and maintenance of transmission networks, including optical fiber communication, IP/MPLS, and microwave transmission. Passing H31-341_V2.5 Exam is a prerequisite for obtaining the HCIP-Transmission certification, which is recognized by organizations worldwide.

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Huawei HCIP-Transmission V2.5 Sample Questions (Q68-Q73):

NEW QUESTION # 68

Regarding the bit error test of the entire network during commissioning, which of the following statements is correct? (Multiple choice)

- A. During the single-channel bit error test, the remote end needs to perform a hardware loopback on the client side of the tributary board
- B. In order to ensure the smooth passage of services on all channels in the entire network, a 10-minute bit error prediction is required for each single channel
- C. The duration of the full-channel bit error test is 20 hours. If a bit error occurs within 20 hours, it needs to be retested for 20 hours after troubleshooting
- D. It is recommended that the number of OTU single boards or tributary boards connected in series should not exceed 13 during full-channel bit error testing

Answer: A,B,D

NEW QUESTION # 69

Which of the following descriptions about the EX40 board is wrong?

- A. Enhanced 40-wave multiplexer/demultiplexer board
- B. The board supports online optical power monitoring
- C. Single board supports application in dual-fiber bidirectional system
- D. The maximum difference in insertion loss of each channel is not more than 5dB

Answer: D

Explanation:

Explanation:

NEW QUESTION # 70

In a troubleshooting scenario, maintenance personnel are generally required to collect and save on-site evidence before troubleshooting. Which of the following descriptions about collecting and saving on-site evidence is correct?

- A. Periodic collection when the equipment is in normal operation and collection when the equipment fails
- B. Only need data collection when equipment failure
- C. Periodic data collection during normal equipment operation is not necessary
- D. Periodic data collection is only required when the equipment is in normal operation

Answer: A

NEW QUESTION # 71

MAC loopback and PHY loopback are used for fault location. When the MAC loopback is set to in loop, the PHY loopback can be set according to the actual situation.

- A. True
- B. False

Answer: B

NEW QUESTION # 72

Which of the following descriptions about MPLS and PWE3 are correct? (Multiple choice)

- A. A tag stack is a sorted collection of tags. Theoretically, MPLS labels can be nested infinitely.
- B. MS-PW is carried by one PSN tunnel, while SS-PW is carried by multiple PSN tunnel segments.
- C. LSP contains different nodes, divided into Ingress, Transit and Egress, each LSP must contain the above three nodes.
- D. Multiple PW channels can be carried on one or more PSN Tunnels.

Answer: A,D

NEW QUESTION # 73

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