

# Valid Nokia SRAN-Radio-Network-Performance-Optimization Exam Question Free Updates For 1 year

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JAIN COLLEGE, J C Road Bangalore  
Mock Paper -1, December- 2017  
II PUC- Computer Science (41)

Time: 3 Hours 15 Minutes

Max. Marks: 70

## PART - A

I. Answer all the questions. Each question carries ONE mark. 1 x 10 =10

1. What is cache memory?
2. What is a logic gate?
3. What is a binary tree?
4. Mention the operator used to access members of a class.
5. What is address operator?
6. Which command is used to delete a table?
7. Name any one guided transmission media.
8. What is a bandwidth?
9. What is freeware?
10. What are form elements?

## PART - B

II. Answer any FIVE questions. Each Question carries TWO marks. 5 x 2 = 10

11. What is POS form?
12. Prove that  $X+XY=X$ .
13. Write any two advantages of object oriented programming.
14. What is constructor overloading?
15. What are the different file opening modes?
16. What is generalization?
17. Explain primary key constraint with example.
18. What is Circuit Switching?

## PART - C

III. Answer any FIVE questions. Each Question carries THREE marks. 5x 3 = 15

19. Explain the features of XT and AT motherboards.
20. Explain NAND and NOR gate.
21. List the operations performed on Queues.
22. Give the difference between static and dynamic memory.
23. Explain the significance of f stream, h header file.
24. Explain random file organization.
25. What is internet surfing?
26. Explain Radio button, Dropdown, Checkbox.

## PART - D

IV. Answer any SEVEN questions. Each question carries FIVE marks. 7 x 5 = 35

27. State and prove De-Morgan's theorem.
28. Write an algorithm to insert an element into an array.
29. Write an algorithm to POP an element from the stack.
30. Explain the basic concepts of OOP's.
31. Explain the use of an array of object with an example?

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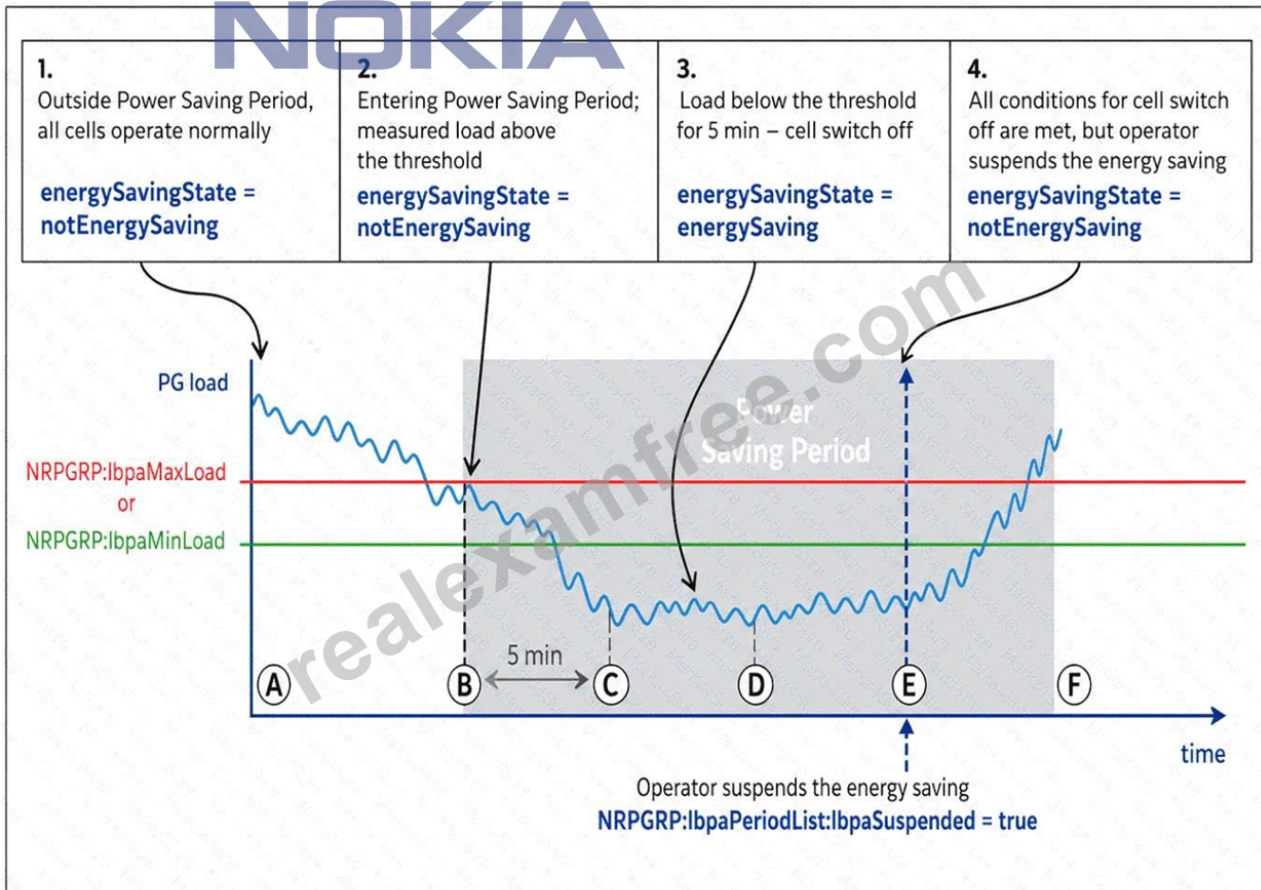
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## Nokia MN: NCSS NPS - SRAN Radio Network Performance Optimization Certification Exam | GS40-NPS-SRPER-E-S03-2510 Sample Questions (Q16-Q21):

### NEW QUESTION # 16

At which mark are all conditions fulfilled for energy saving, so the cell will be switched off? Refer to the image below.



- A. D
- **B. C**
- C. E
- D. B

**Answer: B**

Explanation:

The correct answer is B, which corresponds to mark C in the diagram.

In the diagram, the energy-saving process follows these stages:

Before mark B, the cell is entering the power-saving period, but the required load condition has not yet been satisfied for the required duration.

At mark B, the measured load has gone below the configured threshold, but the cell is not switched off immediately. The system must confirm that the load remains below the required threshold for the configured time.

At mark C, the load has stayed below the required threshold for the required duration, shown as 5 minutes in the diagram. At this point, all conditions are fulfilled, and the cell can be switched off. The diagram also shows the state becoming:

`energySavingState = energySaving`

Marks D and E are already inside or near the later part of the power-saving period. Mark E is associated with operator

suspension/exit behavior, not the initial moment when all switch-off conditions are first fulfilled.

#### NEW QUESTION # 17

Evaluate the impact of using MantaRay SON's centralized PCI optimization module compared to distributed SON implementations.

- A. Distributed SON implementations are more effective in handling multi-vendor networks.
- B. Centralized PCI optimization offers less visibility into network configuration.
- C. Distributed SON implementations provide better optimization benefits.
- **D. Centralized PCI optimization can identify and resolve PCI issues across multi-vendor networks.**

**Answer: D**

Explanation:

The correct answer is C .

A centralized SON approach has broader network-level visibility than a purely distributed implementation.

For PCI optimization , this is important because PCI conflicts, PCI confusion, and neighbor-related PCI issues are not always local to one cell. They may involve several neighboring cells, multiple layers, and sometimes multiple vendors.

Nokia positions MantaRay SON as a centralized, AI-powered network optimization and automation platform for improving RAN quality, efficiency, and customer experience. Nokia also describes MantaRay SON as supporting multi-supplier/non-real-time RIC functionality in the MantaRay SMO framework.

So compared with distributed SON, centralized PCI optimization can better analyze wider-area PCI relationships and resolve PCI problems across multi-vendor or multi-layer networks.

Therefore, the correct statement is:

Centralized PCI optimization can identify and resolve PCI issues across multi-vendor networks.

#### NEW QUESTION # 18

What is the role of admission control in 5G networks ?

- A. To manage the modulation schemes
- B. To optimize the downlink throughput
- **C. To check whether there are enough resources for new connections**
- D. To increase the spectral efficiency

**Answer: C**

Explanation:

The correct answer is D .

Admission control is responsible for deciding whether a new UE connection, bearer, service, or session can be accepted by the cell without negatively affecting existing users and services.

In 5G RAN, admission control checks available radio resources such as:

- \* PRB availability
- \* PDCCH/PUCCH capacity
- \* UE context capacity
- \* QoS requirements
- \* GBR or non-GBR bearer requirements
- \* Cell load and congestion status

It does not directly manage modulation schemes; that is handled by link adaptation. It also does not directly increase spectral efficiency or optimize downlink throughput, although good admission control indirectly protects user experience and avoids overload.

So the main role of admission control is:

To check whether there are enough resources for new connections.

#### NEW QUESTION # 19

A customer complains about coverage reduction after modernization of 4G TDD 4x4 sites to Concurrent mMIMO B41/n41 with AEHC module 64T64R . Which of the following statements regarding RF design changes to increase the coverage is correct?

- A. 5G tilt is controlled by tiltOffset , while the 4G tilt is controlled by mMIMOSecSectorBFFProfName .
- B. 5G tilt is controlled by tiltOffset , while the 4G tilt uses the AEHC RET control port.

- C. For concurrent mode, the tilt for both 4G and 5G is controlled by tiltOffset .
- **D. For concurrent mode, the tilt for both 4G and 5G is controlled by mMIMOSecSectorBFProfName and beamforming weight profile parameters.**

**Answer: D**

Explanation:

The correct answer is C .

In a Concurrent mMIMO B41/n41 AEHC 64T64R deployment, LTE and NR share the same Massive MIMO active antenna system. Public Nokia material identifies AEHC as an AirScale Massive MIMO 64T64R B41 radio product, and Nokia's AirScale Massive MIMO portfolio is designed for high-capacity 5G /RAN deployments.

Unlike a passive antenna system where coverage is changed mainly by physical/electrical RET, Massive MIMO coverage is strongly influenced by beamforming profiles and beamforming weight parameters . In concurrent LTE/NR operation, the RF design must consider the shared active antenna behavior, not independent passive RET-style tilt control per technology.

Therefore, for concurrent 4G/5G mMIMO coverage adjustment, the correct statement is:

The tilt/coverage behavior for both 4G and 5G is controlled by mMIMOSecSectorBFProfName and beamforming weight profile parameters.

### NEW QUESTION # 20

In the context of mmWave deployments , what is the primary benefit of analog beamforming ?

- A. It increases the number of supported UEs per sector.
- **B. It offers low cost and low complexity for coverage at higher mmWave bands.**
- C. It supports multiple beams per antenna panel.
- D. It allows dynamic beam switching between different panels.

**Answer: B**

Explanation:

The correct answer is A .

In mmWave systems, beamforming is essential because FR2 signals suffer from high path loss and blockage.

Analog beamforming uses fewer RF chains than fully digital beamforming, so it is cheaper and less complex in terms of RF hardware, power consumption, and implementation. Research and industry references explain that analog and hybrid beamforming architectures reduce the number of required ADCs/RF chains compared with fully digital beamforming, lowering cost and power complexity.

However, analog beamforming has limitations. It usually forms one beam, or a limited number of beams, at a time from a panel.

Supporting multiple independent simultaneous beams and more UEs per sector is more associated with digital or hybrid beamforming, not pure analog beamforming.

Therefore, the primary benefit is:

Low cost and low complexity for coverage at higher mmWave bands.

### NEW QUESTION # 21

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