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Pure Storage Certified FlashArray Implementation Specialist Sample Questions (Q107-Q112):

NEW QUESTION # 107

An Implementation Engineer performing an inventory of hardware for an NDU from an FlashArray//X70R2 to an FlashArray //X90R3 and discovers a complete FlashArray//X90R3 was shipped in place of an NDU kit. What is the minimum hardware the Implementation Engineer should use from this array to complete the NDU?

- A. Controller and NVRAM
- B. Controller and power supplies
- C. Controllers

Answer: A

Explanation:

To complete the Non-Disruptive Upgrade (NDU) from an //X70R2 to an //X90R3 using a fully shipped array, the Implementation Engineer must extract and use the Controllers and NVRAM modules.

Stateless Architecture: The FlashArray upgrade philosophy allows the chassis and data drives (Data Packs) to remain in place while the compute (controllers) and cache (NVRAM) are swapped.

Component Compatibility:

Controllers: The X90R3 controllers provide the upgraded CPU/performance required.

NVRAM: The NVRAM modules (located in the chassis front or rear slots depending on generation, but logically paired with controller performance) often differ between generations (R2 vs. R3) and performance tiers (70 vs. 90). The X70R2 NVRAM modules are typically not compatible with X90R3 controllers. Therefore, the engineer must swap the NVRAM modules along with the controllers to ensure the write cache is recognized.

Power Supplies: Both the X70R2 and X90R3 utilize the same high-capacity 1600W Power Supply Units. Unless the existing PSUs are faulty, they do not technically need to be swapped for the system to function, making "Controller and NVRAM" the precise minimum requirement for the logic upgrade.

NEW QUESTION # 108

At the completion of an upgrade, which command is used to send phonehome logs?

- A. purearray phonehome --send-today
- B. purearray phonehome --send-logs
- C. purelogs --send-all

Answer: B

Explanation:

The command purearray phonehome --send-logs is used to send all Phonehome logs to support upon completion of an upgrade.

NEW QUESTION # 109

Which default speed FC SFPs are shipped in FlashArray//XCR4 FC arrays?

- A. 0
- B. 1
- C. 2

Answer: C

Explanation:

FlashArray//X R4 (and the associated //C R4) models, introduced with the PCIe Gen 4 architecture, feature support for 64Gb Fibre Channel (FC) connectivity. The default Fibre Channel Host Bus Adapters (HBAs) and the accompanying SFPs shipped with this generation are 64Gb capable, marking a significant performance leap from the 32Gb standard of the R3 generation.

While the HBAs are backward compatible with 32Gb and 16Gb SAN infrastructures, the R4 platform is explicitly marketed and technically defined by its ability to drive 64Gb line speeds to match the increased throughput of the backend PCIe Gen 4 bus and DirectFlash Modules. For an Implementation Engineer verifying the bill of materials or configuring the switch ports, it is critical to know that the R4 optics are native 64Gb. If the customer's switch infrastructure is only 16Gb or 32Gb, the ports will auto-negotiate down, but the shipped default hardware is the 64Gb standard (Option C). Option A (16Gb) and Option B (32Gb) refer to the default standards for the older //M and //X R2/R3 generations respectively.

NEW QUESTION # 110

During a deployment, an Implementation Engineer attempts to name a FlashArray corporate Lab-0L, but the array rejects the name. What is the reason for the failure?

- A. FlashArray names cannot include numbers.
- B. FlashArray names cannot include hyphens.
- C. FlashArray names cannot contain spaces.

Answer: C

Explanation:

Pure Storage enforces strict naming conventions for FlashArrays to ensure compatibility with network protocols (like DNS), replication pairing, and fleet management tools. The allowable character set for an array name is alphanumeric characters (A-Z, a-z, 0-9) and hyphens (-).

The proposed name "corporate Lab-0L" contains a space character between "corporate" and "Lab-0L". FlashArray names cannot contain spaces.

Hyphens (Option A) are allowed and commonly used (e.g., FlashArray-01).

Numbers (Option B) are allowed (e.g., Lab-01).

To resolve the error, the Implementation Engineer should replace the space with a hyphen or remove it entirely (e.g., corporate-Lab-0L or corporateLab-0L). This restriction prevents issues with CLI parsing and host hostname resolution where spaces are often interpreted as delimiters.

NEW QUESTION # 111

Which PCI cards must be validated on the Bill of Materials (BOM) to allow an Implementation Engineer to attach a DirectFlash Shelf to a FlashArray//XL130R5?

- A. FA-XL-100G Ethernet/RoCE 2-Port
- B. FA-XL-200G Ethernet/RoCE 2-Port
- C. FA-XL-100G iSCSI/RoCE 2-Port

Answer: A

Explanation:

The FlashArray//XL series (such as the XL130R5) utilizes a modern backend connectivity architecture for its expansion shelves, specifically the DirectFlash Shelf (DFS). Unlike older generations that might have used SAS (Serial Attached SCSI) for shelf connectivity, the //XL architecture relies on high-speed Ethernet with RoCE (RDMA over Converged Ethernet) to communicate with external media shelves. This ensures that the NVMe drives in the shelf perform with the same low latency as those in the head unit.

To attach a DirectFlash Shelf to a FlashArray//XL130R5, the chassis must be populated with specific PCIe interface cards designated for shelf back-end connectivity. The correct part for this is the FA-XL-100G Ethernet/RoCE 2-Port card. This card provides the necessary 100 Gigabit Ethernet bandwidth and RoCE protocol support required for the NVMe-oF (NVMe over Fabrics) backend fabric that Pure Storage uses.

It is crucial to distinguish this from host-facing cards. While an "iSCSI/RoCE" card exists, the shelf connectivity specifically utilizes the "Ethernet/RoCE" designation in the BOM to differentiate backend fabric ports from frontend host ports. The 200G options are generally reserved for host connectivity or inter-array clustering in specific high-performance setups, but the standard validated card for connecting the DFS to the //XL130R5 is the 100G Ethernet/RoCE adapter. Verifying this line item on the BOM prevents onsite delays where the engineer might otherwise find themselves with incompatible ports for the expansion shelves.

NEW QUESTION # 112

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