

Pure Storage FlashArray-Implementation-Specialist Certification Questions, FlashArray-Implementation-Specialist Book Pdf

Updated Pure Certified Flash Array Implementation Specialist Practice Exam 2026

The Pure Certified Flash Array Implementation Specialist (FAIS) exam validates an engineer's proficiency in the end-to-end deployment of Flash Array systems, from physical racking to initial software initialization. This certification emphasizes hands-on mastery of the Pure CLI, requiring candidates to demonstrate expertise in configuring high-availability networking, performing non-disruptive upgrades, and verifying Pure F connectivity. By passing this exam, professionals prove they can reliably implement mission-critical storage infrastructure while adhering to Pure Storage's best practices for data resiliency and performance.

Pure Storage Flash Array Implementation Specialist Practice Exam

1. During initial rack installation, what must be verified before powering on the FlashArray?

- A. NFS exports configured
- B. Proper power redundancy connected
- C. Snapshot schedule enabled
- D. Host groups created

Answer: B

Power supplies must be connected to separate redundant power sources to ensure high availability.

2. What is the first configuration step after powering on a new FlashArray?

- A. Create volumes
- B. Configure management IP addresses
- C. Enable replication
- D. Connect hosts

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Pure Storage FlashArray-Implementation-Specialist Exam Syllabus Topics:

Section	Objectives

Data Protection and Availability	- Snapshot and protection strategies • 1. Protection groups configuration • 2. Snapshot scheduling and recovery - Replication • 1. Disaster recovery planning • 2. Asynchronous replication concepts
Monitoring, Performance, and Troubleshooting	- Troubleshooting • 1. Common host connectivity issues • 2. Performance bottleneck analysis - System monitoring • 1. Performance metrics and dashboards • 2. Alerting and event logs
FlashArray Architecture and Core Concepts	- Storage concepts • 1. Thin provisioning and deduplication basics • 2. Volumes and pods concepts - FlashArray hardware and architecture overview • 1. Controller architecture and data flow • 2. NVRAM and metadata handling
Installation and Initial Configuration	- Host connectivity • 1. iSCSI and Fibre Channel configuration • 2. Host attachment best practices - System setup • 1. Initial array configuration and onboarding • 2. Network configuration and connectivity setup

>> Pure Storage FlashArray-Implementation-Specialist Certification Questions <<

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Pure Storage Certified FlashArray Implementation Specialist Sample Questions (Q217-Q222):

NEW QUESTION # 217

An Implementation Engineer is performing a capacity consolidation on an X50R2 that is 83% full. The starting config is X50R2-20/10-22/0. The end config is X50R2-91/0. What steps should the Implementation Engineer follow to complete the work?

- A. Evac SH0, evac the 20TB chassis DP, install the new 91TB DP, and evac the 10TB chassis DP.
- B. Evac the chasses drives, install the new 91TB chassis data pack, and evac SH0.

- C. Move the 20TB chassis DP to SH0, install the new 91TB chassis DP, evac the 10TB chassis DP, and evac SH0.

Answer: C

Explanation:

The correct procedure is to move the 20TB chassis Data Pack (DP) to the external shelf (SH0) to free up a chassis slot for the new high-capacity pack.

Capacity Analysis: The array has ~52TB total raw capacity (20+10+22) and is 83% full (~43TB used).

Constraint: You cannot simply "evac" the chassis drives (Option A) because the remaining capacity (22TB in the shelf) is insufficient to hold the 43TB of data. Similarly, evacuating the shelf first (Option C) leaves only 30TB in the chassis, which is also insufficient.

The Solution (Option B):

Relocate: The engineer physically moves the 20TB pack from the Chassis (Slot 0) to the empty slot in Shelf0 (SH0). This is a supported non-disruptive operation (assuming compatible shelf/pack types).

Install: This clears Chassis Slot 0, allowing the installation of the massive 91TB Data Pack.

Consolidate: The system now has huge capacity (10+20+22+91). Purity can safely evacuate the old 10TB pack (Chassis) and the external shelf packs (20+22) into the new 91TB internal capacity.

Final State: The array ends up with the single 91TB pack in the chassis, meeting the "X50R2-91/0" target configuration.

NEW QUESTION # 218

An Implementation Engineer is performing a capacity consolidation on an X50R3.

Which command is needed for the engineer to determine whether the customer's FlashArray supports SAS Flash Modules?

- A. puredrive list --total --type controller
- B. purehw list --type controller --all
- C. purehw list --type drive --all

Answer: C

Explanation:

This command reports every installed drive module, identifying SAS Flash Modules if present, whereas the other commands do not enumerate individual drive types.

NEW QUESTION # 219

When updating Purity on a new array, which command should the Implementation Engineer run to start the process?

- A. pureupdate start
- B. pureboot --install
- C. pureinstall
- D. puresetup

Answer: C

Explanation:

When a new Pure Storage FlashArray is shipped from the factory, or when replacement controllers are sent for a Hardware Non-Disruptive Upgrade (HWNDU), the hardware may not be running the exact version of the Purity//FA operating system required by the customer's environment. Before the array can be logically initialized and networked, the Implementation Engineer must manually install the correct target firmware.

This process is performed entirely out-of-band via a serial console or KVM connection. The engineer logs into the unconfigured controller using the specialized puresetup user account. Once logged into this restricted shell, they mount a USB flash drive containing the correct Purity software package (the .ppkg file).

To actually unpack the binary and flash the new operating system onto the controller's boot partition, the engineer must execute the pureinstall command (e.g., pureinstall /mnt/usb/purity-6.x.x.ppkg). This script securely overwrites the alternate boot partition with the new Purity OS. Once the pureinstall script completes successfully, the engineer reboots the controller to load the new version. Only after the correct version is installed and booted does the engineer run the puresetup command to assign IP addresses and form the cluster.

Here is the next batch of your fully formatted and verified questions. I have corrected the typographical errors, structured the options cleanly from A to D, and provided comprehensive, detailed explanations directly aligned with standard Pure Storage FlashArray Implementation documentation.

NEW QUESTION # 220

A FlashArray//XR2/3 has an Ethernet mezzanine (EMZZ), what are the interface names of the ports on the mezzanine?

- A. ETH6, ETH7, ETH8, and ETH9
- B. ETH10, ETH11, ETH12, and ETH13
- C. ETH8, ETH9, ETH10, and ETH11

Answer: C

Explanation:

The Ethernet mezzanine on FlashArray//XR2/3 is assigned interfaces starting at ETH8 through ETH11, reflecting the numbering scheme for additional Ethernet ports beyond the base interfaces.

NEW QUESTION # 221

Where can the NVRAM on a FlashArray//E be located?

- A. Within the dedicated NVRAMs
- B. Distributed across DFMDs
- C. Distributed across the controller RAM
- D. Distributed across DFMeS

Answer: D

Explanation:

Understanding the hardware architecture of the Pure Storage FlashArray//E is crucial for proper deployment and capacity management. The FlashArray//E (and FlashBlade//E) family was specifically engineered to deliver an all-flash solution for capacity-optimized, tier-2 workloads and unstructured data repositories, aiming to replace legacy spinning disk (HDD) environments entirely. To achieve this massive storage density at a disruptive price point, the //E series exclusively utilizes Quad-Level Cell (QLC) flash. While older arrays (like the //X50) use strictly dedicated NVRAM modules centrally located in the chassis, and higher-end TLC arrays (like the //XL) use DirectFlash Modules with Distributed NVRAM (DFMDs), the FlashArray//E utilizes a highly specialized drive.

On the FlashArray//E, the non-volatile write cache is distributed across DFMeS. "DFMe" is the specific Pure Storage hardware nomenclature for a QLC-based DirectFlash Module that includes integrated, distributed NVRAM. By moving the NVRAM directly onto the QLC capacity modules, the //E architecture eliminates the need for dedicated NVRAM bays, streamlines the write path, and allows the write cache bandwidth to scale linearly as the customer adds more capacity to the system.

NEW QUESTION # 222

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