

Pure Storage FlashArray-Implementation-Specialist Test Review - FlashArray-Implementation-Specialist Sample Exam

Updated Pure Certified Flash Array Implementation Specialist Practice Exam 2026

The Pure Certified Flash Array Implementation Specialist (FAIS) exam validates an engineer's proficiency in the end-to-end deployment of Flash Array systems, from physical racking to initial software initialization. This certification emphasizes hands-on mastery of the Pure CLI, requiring candidates to demonstrate expertise in configuring high-availability networking, performing non-disruptive upgrades, and verifying Pure1 connectivity. By passing this exam, professionals prove they can reliably implement mission-critical storage infrastructure while adhering to Pure Storage's best practices for data resiliency and performance.

Pure Storage Flash Array Implementation Specialist Practice Exam

1. During initial rack installation, what must be verified before powering on the FlashArray?

- A. NFS exports configured
- B. Proper power redundancy connected**
- C. Snapshot schedule enabled
- D. Host groups created

Answer: B

Power supplies must be connected to separate redundant power sources to ensure high availability.

2. What is the first configuration step after powering on a new FlashArray?

- A. Create volumes
- B. Configure management IP addresses**
- C. Enable replication
- D. Connect hosts

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Pure Storage FlashArray-Implementation-Specialist Exam Syllabus Topics:

Section

Objectives

	- Host connectivity • 1. Host attachment best practices • 2. iSCSI and Fibre Channel configuration
Topic 1: Installation and Initial Configuration	- System setup • 1. Network configuration and connectivity setup • 2. Initial array configuration and onboarding - Replication • 1. Disaster recovery planning • 2. Asynchronous replication concepts
Topic 2: Data Protection and Availability	- Snapshot and protection strategies • 1. Snapshot scheduling and recovery • 2. Protection groups configuration - FlashArray hardware and architecture overview • 1. Controller architecture and data flow • 2. NVRAM and metadata handling
Topic 3: FlashArray Architecture and Core Concepts	- Storage concepts • 1. Thin provisioning and deduplication basics • 2. Volumes and pods concepts - Troubleshooting • 1. Common host connectivity issues • 2. Performance bottleneck analysis
Topic 4: Monitoring, Performance, and Troubleshooting	- System monitoring • 1. Performance metrics and dashboards • 2. Alerting and event logs

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Pure Storage Certified FlashArray Implementation Specialist Sample Questions (Q189-Q194):

NEW QUESTION # 189

What is the redundancy of the FlashArray//XL PSUs?

- A. 2+2
- B. 1+1
- C. 3+1

Answer: A

Explanation:

The FlashArray//XL features a robust 2+2 power supply unit (PSU) redundancy configuration. Unlike the smaller FlashArray//X chassis (3U), which typically utilizes two power supplies in a 1+1 redundancy setup, the FlashArray//XL utilizes a larger 5U chassis designed for higher performance and density, requiring a more substantial power infrastructure.

The //XL chassis is equipped with four physical Power Supply Units. These are configured to operate in an N+2 mode (effectively 2+2), meaning the system requires two PSUs to support the full electrical load of the chassis, while the other two provide redundancy. This architecture allows the array to survive the simultaneous failure or loss of input power to up to two power supplies without any interruption to service.

This design ensures maximum availability even in scenarios where an entire power grid feed (A-side or B-side) fails, or if multiple hardware components malfunction simultaneously. Options A (1+1) applies to the standard //X series, and Option C (3+1) is not a supported configuration for the FlashArray//XL. The 2+2 design is critical for maintaining the "Always-On" reliability standards required for the mission-critical workloads that the //XL platform supports.

NEW QUESTION # 190

When using FA File, where can the Implementation Engineer manage directories?

- A. Storage > File Systems
- B. Settings > Access > Directory Services
- C. Analytics > Performance > Directories

Answer: A

Explanation:

In the Purity//FA Graphical User Interface (GUI), the management of FA File resources is integrated into the core "Storage" navigation pane. To manage specific file system directories-such as creating new exports, setting quotas, or managing directory-level policies-the Implementation Engineer must navigate to Storage > File Systems .

Within this view, the engineer can see the list of provisioned file systems. By clicking into a specific file system, they can access the "Directories" sub-tab or detailed view where folders (directories) within that file system can be explicitly managed.

It is important not to confuse this with "Directory Services," which is located under the Settings menu.

"Directory Services" (Option B) refers to the configuration of Active Directory (AD) or LDAP connections for user authentication and identity mapping, not the management of the actual file storage structure.

Similarly, the Analytics tab (Option C) provides performance metrics but does not offer configuration capabilities for creating or modifying directories.

NEW QUESTION # 191

An Implementation Engineer is setting up a customer environment and has specified a number of NTP servers. Which command should the Implementation Engineer run to list the NTP servers that have been configured?

- A. purenetwork list --ntpserver
- B. puredns list --ntpserver
- C. purearray list --ntpserver

Answer: C

Explanation:

To view the currently configured Network Time Protocol (NTP) servers on a FlashArray, the correct command is purearray list --ntpserver.

Command Hierarchy: While NTP is a networking function, in the Purity CLI object model, time synchronization is considered a global array attribute rather than a specific interface setting. Therefore, it falls under the purearray command namespace.

Usage: Running purearray list --ntpserver returns a list of the IP addresses or FQDNs of the time servers the array is currently using to sync its system clock.

Invalid Options:

purenetwork list (Option B) displays IP interface configurations (IPs, subnets, gateways) but does not list service-level pointers like NTP.

puredns list (Option A) is strictly for DNS nameservers and suffix configurations.

Importance: Verifying NTP is critical for log timestamp accuracy, ActiveCluster mediation, and Kerberos authentication.

NEW QUESTION # 192

When installing the first DirectFlash Shelf on a FlashArray//XR2/3, what mezzanine ports are used on the controllers?

- A. ETH7 and ETH9
- **B. ETH6 and ETH9**
- C. ETH6 and ETH8

Answer: B

Explanation:

When installing the first DirectFlash Shelf (DFS) on a FlashArray//XR2 or //XR3 utilizing the onboard or default mezzanine connectivity, the correct ports to use are ETH6 and ETH9.

Hardware Layout: The FlashArray//XR2 and //XR3 utilize a specific Ethernet Mezzanine (EMZZ) slot for backend connectivity to NVMe-oF shelves. This card typically enumerates its ports as ETH6 through ETH9 (a 4-port configuration).

Cabling Rule: To maximize physical separation and ensure redundancy across the internal ASIC lanes of the mezzanine card, standard cabling procedures for the first shelf dictate using the outer ports: ETH6 (Port 0) and ETH9 (Port 3).

Redundancy: This configuration (Port 0 and Port 3) is preferred over using adjacent ports (like 6 and 7) to reduce the risk of a single lane cluster failure affecting both connections to the shelf. The second shelf, if added later, would typically utilize the remaining ports (ETH7 and ETH8).

NEW QUESTION # 193

Which NVRAM bays are always populated on the FlashArray//XR2/3?

- **A. 0-1**
- B. 0-2
- C. 1-2
- D. 0-3

Answer: A

Explanation:

On standard Pure Storage FlashArray models, including the //XR2 and //XR3 generations, the hardware architecture relies heavily on dedicated, non-volatile memory (NVRAM) modules. These modules are responsible for safely ingesting incoming host writes and protecting data in flight against unexpected power loss or controller failures before the data is ultimately destaged to the capacity flash drives.

To maintain absolute high availability and guarantee there is no single point of failure within the write cache, this NVRAM must always be mirrored across separate physical modules. During the physical hardware installation, the Implementation Engineer populates the dedicated NVRAM bays located centrally on the front bezel of the chassis. According to official Pure Storage implementation guidelines, the primary pair of NVRAM modules must always be installed in the first two designated bays, which are explicitly labeled 0 and 1 (NVB0 and NVB1).

While higher-performance arrays in the series (like the //X70 and //X90) possess enough ingest bandwidth to require populating bays 2 and 3 as well, bays 0 and 1 are the universal minimum baseline that is definitively populated across every array in the series to ensure N+1 cache redundancy.

NEW QUESTION # 194

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