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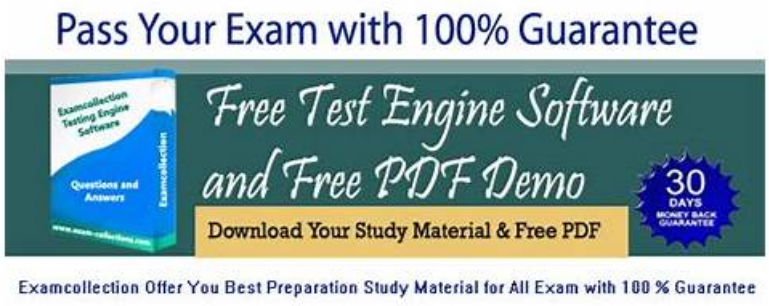

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EXIN CDCS Exam Syllabus Topics:

Topic	Details
Topic 1	• Data Centre Life Cycle and Standards: This section of the exam measures the skills of data center professionals and covers the various stages involved in the life cycle of a data center, from planning and design to implementation and decommissioning.
Topic 2	• Data Centre Environmental Considerations and Efficiency: This section evaluates the proficiency of data center professionals in addressing environmental factors and promoting efficiency within data center operations. The target audience, including data center managers and engineers, will be tested on their ability to identify and implement measures that enhance energy efficiency, cooling management, and sustainable practices.
Topic 3	• Designing and Implementing a Data Centre: In this module, the exam assesses the knowledge of Exin data center professionals tasked with the design and implementation of data centers. Candidates will learn the key principles of creating an efficient data center layout, including considerations for scalability, redundancy, and security.

EXIN EPI Certified Data Centre Specialist Sample Questions (Q103-Q108):

NEW QUESTION # 103
EMF shielding material needs to be installed as EMF levels from the transformer room into the computer room are measured at 100 mG. The transformer room is ~10 meters away, separated by a corridor. Where should shielding be installed?

- A. As close as possible to the transformer room
- B. It does not matter; either close to the transformer room or computer room is okay
- C. Shielding is not required as 100 mG is within acceptable levels
- D. As close as possible to the computer room

Answer: A

Explanation:
The most effective EMF mitigation is to install shielding as close as possible to the source of radiation. By blocking or redirecting magnetic flux at the origin (the transformer room walls), the overall field propagation into adjacent areas is minimized. If shielding were placed at the computer room, the field would already have spread over the intervening space, requiring more material and higher cost.
Standards such as IEEE Std 299 (EMC Shielding Effectiveness) and IEC 61000 emphasize source-based mitigation. Additionally, ANSI/TIA-942 requires EMF shielding where magnetic flux exceeds recommended ICT thresholds (generally <5 mG for sensitive tape/disk storage).
Although 100 mG is often tolerated by modern equipment, legacy magnetic storage can be affected, so shielding is still prudent.
Hence, the correct location is at the transformer room wall.
References: IEEE Std 299 (EMI Shielding), ANSI/TIA-942-B §6.6.4 (EMF Requirements), IEC 61000 EMC standards.

NEW QUESTION # 104
Where should raised-floor installation start?

- A. Point C (center of the room)
- B. Point B (side wall)
- C. Point A (entrance corner)
- D. Point D (corner opposite entrance)

Answer: A

Explanation:
Best practice is to begin raised-floor installation at the center of the room, working outward. This minimizes alignment errors and ensures the tile grid is centered, which is critical for aisle containment and rack alignment.
Starting at the perimeter (A, B, D) causes cutting of tiles along both sides, misalignment with rack rows, and possible airflow inefficiencies. By starting at the center, tiles can be cut symmetrically around the edges, providing better aesthetics, balanced airflow, and structural stability.
Industry guidelines such as CISCA recommend this approach for raised floors in mission-critical spaces.
References: CISCA Raised Access Floor Guidelines, ANSI/TIA-942-B §6.3.

NEW QUESTION # 105
Racks with 1.0 m depth and cold aisle containment with 3 perforated tiles are used. What aisle pitch is recommended?

- A. 10 tiles pitch rule
- B. 7 tiles pitch rule
- C. 8 tiles pitch rule
- D. 5 tiles pitch rule

Answer: B

Explanation:
The aisle pitch is the total width of a rack row plus cold aisle plus rack row. For 1.0 m racks on each side with cold aisle containment, ASHRAE and TIA-942 recommend the 7-tile rule (each tile ~0.6 m). This ensures enough width for equipment clearance, airflow distribution, and human access.
* 5-tile pitch is too narrow, restricting containment effectiveness.

* 8-10 tiles may be used in some hyperscale layouts but are not standard for 1 m racks.
Thus, the correct design recommendation is the 7 tiles pitch rule.
References: ANSI/TIA-942-B §6.3.6 (Aisle Spacing), ASHRAE TC 9.9 "Airflow Management Best Practices."

NEW QUESTION # 106
The humidity in the computer room has changed from about 50% down to 35% Relative Humidity (RH).
What influence does this have on Electrostatic Discharge (ESD)?

- A. Relative humidity has no influence on ESD
- B. ESD levels will go up
- C. ESD levels will go down
- D. No influence as long as the temperature is at approximately 20°C/77°F

Answer: B

Explanation:
As relative humidity decreases, Electrostatic Discharge (ESD) risks increase. Lower humidity levels reduce the amount of moisture in the air, which normally helps dissipate static charges. When the humidity drops from 50% to 35%, the likelihood of static electricity accumulating on surfaces rises, leading to a higher potential for ESD incidents that could damage sensitive IT equipment.
Detailed Explanation:
ESD events are more common in dry environments because there is less atmospheric moisture to neutralize electrical charges. Maintaining relative humidity above 40% helps minimize the risk of ESD, which is why data centers often control humidity levels tightly to protect equipment from static discharge that could cause hardware failures or data loss.
EPI Data Center Specialist References:
EPI data center best practices stress the importance of maintaining stable humidity levels to prevent ESD, particularly in computer rooms. Recommended humidity ranges are typically above 40% to prevent conditions that would foster static buildup.

NEW QUESTION # 107
Which formula is correct for a three-phase system?

- A. Phase-to-Phase Voltage = 1 # (Phase-to-Neutral Voltage ÷ 1.732)
- B. Phase-to-Phase Voltage = 1 # (Phase-to-Neutral Voltage × 1.732)
- C. Phase-to-Phase Voltage = Phase-to-Neutral Voltage ÷ 1.732
- D. Phase-to-Phase Voltage = Phase-to-Neutral Voltage × 1.732

Answer: D

Explanation:
For balanced three-phase systems: where .
References: IEC 60038 (standard voltages), any power systems fundamentals text.

NEW QUESTION # 108
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