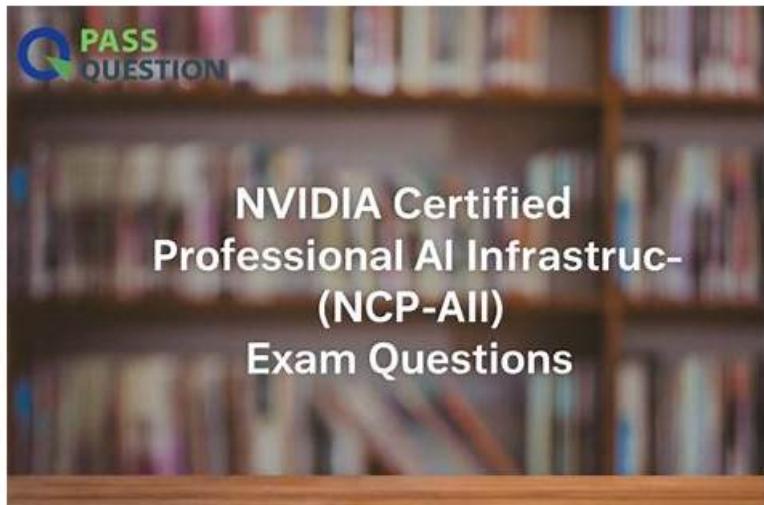


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### NVIDIA AI Infrastructure Sample Questions (Q21-Q26):

#### NEW QUESTION # 21

Consider the following 'ibroute' command used on an InfiniBand host: 'ibroute add dest Ox1a dev ib0'. What is the MOST likely purpose of this command?

- A. To disable routing on the ib0 interface.
- B. To configure a static route for traffic destined to IP address Ox1a, using the InfiniBand interface ib0.
- C. To add a default route for all traffic destined outside the InfiniBand subnet.
- D. To create a static route for traffic destined to LID Ox1a, using the InfiniBand interface ib0.
- E. To configure the MTU size on the ib0 interface to Ox1a bytes.

**Answer: D**

Explanation:

The 'ibroute add dest Ox1a dev ibC' command creates a static route for traffic destined for the InfiniBand LID (Local Identifier) Ox1a, using the InfiniBand interface named 'ib0'. InfiniBand routing is primarily based on LIDS, not IP addresses directly (though IP over IB is possible). The 'dest' parameter specifies the destination LID.

### NEW QUESTION # 22

You are working with a BlueField-3 DPIJ and wish to programmatically control the PCIe link speed and width. Which interface exposes the most direct way to manage these low-level hardware settings on the DPU?

- A. Directly accessing the PCIe configuration space via '/sys/bus/pci/...:.
- B. Modifying the device tree blob (DTB) and rebooting the DPIJ.
- C. The standard Linux ethtool utility.
- D. Using the Mellanox mlxconfig utility or its equivalent.
- E. The NVIDIA Management Library (NVML).

**Answer: D**

Explanation:

The 'mlxconfig' utility (or its equivalent for newer BlueField generations) is specifically designed for configuring Mellanox/NVIDIA network adapters, including BlueField DPUs. It provides access to a wide range of hardware settings, including PCIe link speed and width. NVML is primarily for GPU management, not DPIJ configuration. 'ethtool' manages network interface settings, but not PCIe link parameters. Accessing the PCIe configuration space directly is possible but complex and risky. Modifying the DTB is a more permanent and low-level approach, typically used during initial board configuration, not for runtime adjustments.

### NEW QUESTION # 23

You are using NVIDIA Spectrum-X switches in your A1 infrastructure. You observe high latency between two GPU servers during a large distributed training job. After analyzing the switch telemetry, you suspect a suboptimal routing path is contributing to the problem. Which of the following methods offers the MOST granular control for influencing traffic flow within the Spectrum-X fabric to mitigate this?

- A. Implement Adaptive Routing (AR) or Dynamic Load Balancing (DLB) features available on the Spectrum-X switches to dynamically adjust paths based on network conditions.
- B. Manually configure static routes on the Spectrum-X switches to force traffic between the GPU servers along a specific path.
- C. Adjust the Equal-Cost Multi-Path (ECMP) hashing algorithm globally on all switches.
- D. Configure QOS (Quality of Service) policies to prioritize traffic from the high-latency GPU servers.
- E. Disable IPv6 to simplify routing decisions.

**Answer: A**

Explanation:

Adaptive Routing (AR) and Dynamic Load Balancing (DLB) are features specifically designed to dynamically adjust paths based on real-time network conditions in Spectrum-X. This provides the most granular and automated way to respond to congestion and optimize traffic flow compared to static routing or global ECMP adjustments. QOS prioritizes, but doesn't change the chosen path.

### NEW QUESTION # 24

You are managing an A1 infrastructure based on NVIDIA Spectrum-X switches. A new application requires strict Quality of Service (QOS) guarantees for its traffic. Specifically, you need to ensure that this application's traffic receives preferential treatment and minimal latency.

What combination of Spectrum-X features and configurations would be MOST effective in achieving this?

- A. Increase the MTU size on all interfaces to reduce packet fragmentation and overall latency.
- B. Configure DiffServ Code Point (DSCP) marking on the application's traffic, map these DSCP values to specific traffic classes within the Spectrum-X switch, and configure Weighted Fair Queueing (WFQ) or Strict Priority Queueing on the egress ports.
- C. Use VLAN tagging to isolate the application's traffic into a separate virtual network.
- D. Disable Adaptive Routing (AR) to ensure that traffic always takes the shortest path.
- E. Enable broadcast storm protection.

## Answer: B

Explanation:

DSCP marking, traffic class mapping, and WFQ/Strict Priority Queueing are fundamental QOS mechanisms. DSCP marking allows you to classify traffic based on application requirements. Traffic classes within the switch provide different levels of service. WFQ and Strict Priority Queueing ensure that high-priority traffic receives preferential treatment on egress ports. The other options are less relevant to QOS guarantees for a specific application.

## NEW QUESTION # 25

Which of the following storage technologies are most suitable for storing large training datasets used in deep learning, considering both performance and cost?

- A. High-performance NVMe SSDs in a local RAID configuration
- **B. A parallel file system (e.g., BeeGFS, Lustre) deployed on NVMe SSDs**
- C. Object storage (e.g., AWS S3, Azure Blob Storage) accessed directly from the training nodes
- D. Tape backup systems
- E. SATA HDDs in a network-attached storage (NAS) configuration

## Answer: B

Explanation:

NVMe SSDs in a local RAID offer high performance and relatively low latency, making them suitable for data that needs to be accessed quickly. Parallel file systems deployed on NVMe SSDs provide the highest performance and scalability, especially for large datasets accessed concurrently by multiple training nodes. Object storage can be used for initial data ingest or archival but is generally slower than local or parallel file systems for training. SATA HDDs and Tape backup systems are a low performing option for this case.

## NEW QUESTION # 26

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