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NVIDIA AI Infrastructure Sample Questions (Q35-Q40):

NEW QUESTION # 35

You're monitoring the storage I/O for an AI training workload and observe high disk utilization but relatively low CPU utilization. Which of the following actions is LEAST likely to improve the performance of the training job?

- A. Reducing the number of parallel data loading threads.
- B. Switching from HDDs to NVMe SSDs.
- C. Increasing the batch size of the training job.
- D. Implementing data prefetching to load data into memory before it's needed.
- E. Adding more RAM to the system.

Answer: A

Explanation:

High disk utilization and low CPU utilization indicate an I/O bottleneck. Switching to faster storage (A), prefetching data (B), increasing the batch size (C), and adding more RAM (D) can all help alleviate the I/O bottleneck. Reducing the number of parallel data loading threads (E) would likely worsen the bottleneck by underutilizing the available I/O bandwidth.

NEW QUESTION # 36

After ClusterKit reports "GPU-Host latency exceeds threshold," which NVIDIA diagnostic tool should be used to isolate hardware faults?

- A. Re-run ClusterKit with `--stress=gpu -Y 60` to extend test duration
- B. `nvidia-smi topo -m` to inspect GPU topology connections
- C. DCGM Diags `dcgmi diag -r 2`
- D. `ib_write_bw` to measure InfiniBand bandwidth between nodes

Answer: B

Explanation:

"GPU-Host latency" issues in NVIDIA DGX or HGX systems are frequently caused by incorrect PCIe affinity or sub-optimal NUMA (Non-Uniform Memory Access) mapping. If a GPU is forced to communicate with a CPU core or an HCA that is not on its local PCIe switch/root complex, latency increases significantly as data must cross the QPI/UPI inter-processor links. The command `nvidia-smi topo -m` provides a detailed matrix of the system's internal topology, showing how GPUs, CPUs, and NICs are connected. It identifies whether the connection is via a single PCIe switch (PIX), multiple switches (PXB), or across the CPU (SYS).

By inspecting this map, an administrator can identify if a software process is pinned to the wrong NUMA node or if a hardware path is unexpectedly degraded. While DCGM (Option C) is good for checking component health, it doesn't map the logical-to-physical affinity paths that cause specific latency "threshold" warnings.

NEW QUESTION # 37

You are tasked with upgrading the NVIDIA driver on a Kubernetes node hosting GPU-accelerated AI workloads. To minimize downtime and ensure a smooth transition, which sequence of steps should you follow?

- A. **Cordon the node, upgrade the driver, reboot the node, and uncordon it.**
- B. Upgrade the NVIDIA container toolkit, then upgrade the driver, reboot the node, and uncordon it.
- C. Delete all pods running on the node, upgrade the driver, reboot the node, and recreate the pods.
- D. Upgrade the driver directly on the node, reboot the node, and let Kubernetes automatically reschedule the workloads.
- E. Drain the node, upgrade the driver, reboot the node, and uncordon it.

Answer: A

Explanation:

Cordoning the node prevents new pods from being scheduled on it. After upgrading the driver and rebooting, uncordoning the node allows Kubernetes to resume scheduling workloads. Draining the node before upgrading can cause unnecessary downtime if pods are migrated before the upgrade process starts. The NVIDIA container toolkit must be compatible to the NVIDIA driver, but the upgrade sequence follows Option C steps.

NEW QUESTION # 38

Consider the following simplified CUDA code snippet intended to perform a vector addition:

What are critical steps to validate that this code is correctly utilizing the GPU hardware and producing accurate results?

- A. **All of the above**
- B. Use `'cudaDeviceSynchronize()'` after launching the kernel to ensure the GPU computations are complete before copying data back to the host.
- C. After the `'cudaMemcpy'` from device to host, compare the `'c'` array to a CPU-based vector addition result to verify correctness.
- D. Run the code with `'cuda-memcheck'` to detect memory access errors or race conditions.
- E. Ensure that `'blocksPerGrid'` and `'threadsPerBlock'` are appropriately chosen to maximize GPU occupancy, potentially using the NVIDIA CUDA Occupancy Calculator.

Answer: A

Explanation:

All options highlight important validation steps. Optimizing block/thread configuration maximizes GPU utilization. Verifying results against a CPU-based calculation ensures correctness. 'cudaDeviceSynchronize()' guarantees GPU computations are finished before data transfer. 'cuda-memcheck' detects memory errors. Failing to do any of these could lead to subtle errors or performance bottlenecks.

NEW QUESTION # 39

You are configuring an InfiniBand subnet with multiple switches. You need to ensure that traffic between two specific nodes always takes the shortest path, bypassing a potentially congested link. Which of the following approaches is MOST effective for achieving this using InfiniBand's routing capabilities?

- A. Rely solely on the Subnet Manager's (SM) default path computation algorithm (e.g., Min Hop) without any modifications.
- B. Decrease the MTIJ size on the potential congested link.
- C. Use static routing by manually configuring forwarding tables on each switch along the desired path. This involves specifying DLID-to-Port mappings.
- **D. Utilize the `ibroute` command or similar tool to inject a static route between the nodes, forcing traffic to follow a specific path identified by LID and port number.**
- E. Implement Quality of Service (QOS) to prioritize the traffic between the two nodes, hoping that this will influence the path selection.

Answer: D

Explanation:

Static routing with 'ibroute' (or similar) provides the most direct and reliable way to ensure traffic follows a specific path. The SM's default algorithm might not always choose the optimal path, and QOS only prioritizes traffic, not forces a specific route. Configuring forwarding tables manually on each switch is error-prone and difficult to manage at scale.

NEW QUESTION # 40

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