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All candidates want to get Pure Storage authentication in a very short time, this has developed into an inevitable trend. Each of them is eager to have a strong proof to highlight their abilities, so they have the opportunity to change their current status. It is not easy to qualify for a qualifying exam in such a short period of time. Our company's FlashArray-Implementation-Specialist Study Guide is very good at helping customers pass the exam and obtain FlashArray-Implementation-Specialist certificate in a short time, and now you can free download the demo of our FlashArray-Implementation-Specialist exam torrent from our website. You will love our FlashArray-Implementation-Specialist exam prep for sure.

Pure Storage Certified FlashArray Implementation Specialist Sample

Questions (Q130-Q135):

NEW QUESTION # 130

Before installing array rails, where should the cage nuts be placed?

- A. Lowest square of top RU
- **B. Top square of the lowest RU**
- C. Lowest square of lowest RU

Answer: B

Explanation:

When installing rails for a FlashArray in a rack requiring cage nuts (square hole racks that are not using the tool-less mechanism, or specific threaded adaptations), the correct placement for the cage nut is the top square of the lowest Rack Unit (RU) that the rail will occupy.

Pure Storage rail kits typically secure at the bottom of the targeted rack unit.

* The "3-Hole" Rule: A standard Rack Unit (1U) consists of three vertical holes (often referred to as the bottom, middle, and top hole).

* Alignment: The rail flange is designed to align such that the retaining screw or positioning stud engages specific holes to support the weight. For the standard securement point, the documentation specifies installing the cage nut in the top hole of the specific RU where the rail base sits (often the

"lowest" RU if the device spans multiple U's, though the rail itself is 1U high).

* Precision: Placing it in the "Lowest square of the lowest RU" (Option A) would interfere with the rail's shelf lip or the device below it. Correct alignment ensures the rail is level and the chassis slides in without binding.

NEW QUESTION # 131

A customer has submitted a Pure Engagement Request (PER) / Implementation Workbook (IWB) and the stated array name is "123". What is the issue With this array name?

- A. Array name must be 4 characters or more.
- B. Array name must be alphanumeric.
- **C. Array name cannot start with a digit.**

Answer: C

Explanation:

The Pure Engagement Request (PER) or Implementation Workbook (IWB) is the planning document where customers specify their desired configuration parameters. Validating these parameters against Purity requirements before arriving onsite is a critical step for the Implementation Engineer.

Regarding the array name "123": Array names cannot start with a digit.

Purity naming conventions follow standard DNS hostname RFC guidelines to ensure compatibility with network services and scripts.

While numbers are allowed within the name (e.g., array123), the first character must be a letter.

A name composed entirely of numbers (like "123") or starting with a number (like "1array") can cause ambiguity for parsers that might mistake the name for an IP address, a specific ID, or an integer value in scripts.

The engineer must flag this in the PER and request the customer provide a compliant name (e.g., flasharray-123) to avoid errors during the puresetup initialization process.

NEW QUESTION # 132

What hardware should the Implementation Engineer find in the upgrade kit in order to perform an //X to //XL NDU?

- A. 3 QSFP cables / 4 Mellanox cards / 10 DFMDs
- B. 4 QSFP cables / 2 Mellanox cards / 20 DFMDs
- **C. 2 QSFP cables / 3 Mellanox cards / 20 DFMDs**

Answer: C

Explanation:

The upgrade kit for an //X to //XL Non-Disruptive Upgrade (NDU) includes 2 QSFP cables, 3 Mellanox InfiniBand cards, and 20 DirectFlash Modules (DFMDs) to support new hardware connectivity and capacity requirements.

NEW QUESTION # 133

A customer would like to reduce SafeMode settings for retention and eradication with their current policy. How is authorization obtained to make the requested changes?

- A. Two SafeMode approvers need to acknowledge via Pure1 step-up authentication before any changes can be made.
- B. The local account on FlashArray needs to permit any local changes.
- C. A Pure1 Admin needs to approve any changes for Pure Support to proceed.

Answer: A

Explanation:

To reduce SafeMode protections (such as shortening the retention period or disabling the eradication timer), two authorized SafeMode approvers must authenticate and approve the request via Pure1 step-up authentication .

SafeMode is a ransomware protection feature designed to prevent the accidental or malicious deletion of snapshots. Because reducing these protections weakens the array's security posture, Pure Storage enforces a strict "Ratchet" authorization process.

* The Process: Unlike standard support requests, a single admin or local user cannot authorize this change (making Option C incorrect). The customer must have previously designated specific individuals as "SafeMode Approvers" in their Pure1 portal.

* Authorization: When a request to weaken the policy is made, Pure Support triggers a verification workflow. Two of these designated approvers must log into Pure1 and perform a secondary authentication (often involving a PIN or TOTP) to explicitly "sign off" on the reduction. This "two- person rule" ensures that a compromised credential or a rogue insider cannot unilaterally expose the organization's backup data to destruction.

NEW QUESTION # 134

Where can the NVRAM on a FlashArray//E be located?

- A. Distributed across the controller RAM
- B. Distributed across DFMDs
- C. Distributed across DFMeS
- D. Within the dedicated NVRAMs

Answer: C

Explanation:

Understanding the hardware architecture of the Pure Storage FlashArray//E is crucial for proper deployment and capacity management. The FlashArray//E (and FlashBlade//E) family was specifically engineered to deliver an all-flash solution for capacity-optimized, tier-2 workloads and unstructured data repositories, aiming to replace legacy spinning disk (HDD) environments entirely. To achieve this massive storage density at a disruptive price point, the //E series exclusively utilizes Quad- Level Cell (QLC) flash. While older arrays (like the //X50) use strictly dedicated NVRAM modules centrally located in the chassis, and higher-end TLC arrays (like the //XL) use DirectFlash Modules with Distributed NVRAM (DFMDs), the FlashArray//E utilizes a highly specialized drive.

On the FlashArray//E, the non-volatile write cache is distributed across DFMeS . "DFMe" is the specific Pure Storage hardware nomenclature for a QLC-based DirectFlash Module that includes integrated, distributed NVRAM. By moving the NVRAM directly onto the QLC capacity modules, the //E architecture eliminates the need for dedicated NVRAM bays, streamlines the write path, and allows the write cache bandwidth to scale linearly as the customer adds more capacity to the system.

NEW QUESTION # 135

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