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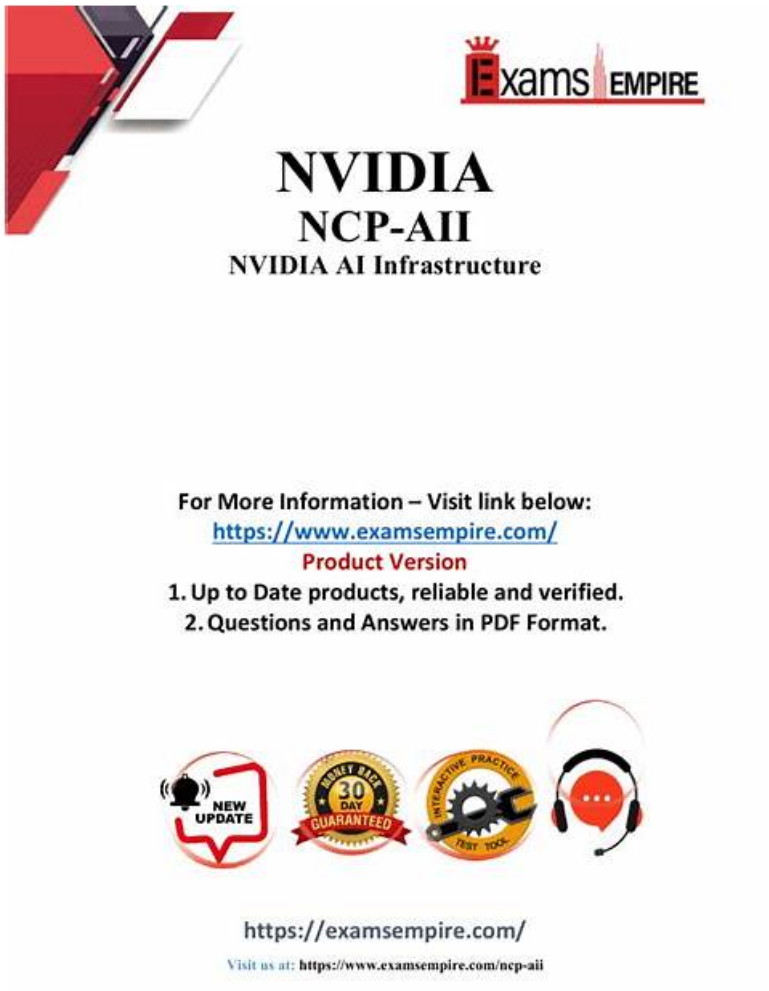

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NVIDIA AI Infrastructure Sample Questions (Q107-Q112):

NEW QUESTION # 107
Consider the following Python code snippet which attempts to extract Digital Optical Monitoring (DOM) data from a transceiver using a hypothetical library 'transceiver_utils'. The transceiver is connected to port 'eth0'. However, the code consistently throws a 'TransceiverError: Invalid port' exception. What is the MOST likely cause of this error?

- A. The fiber cable connected to the transceiver is damaged.
- B. The Python code requires root privileges to access transceiver data.
- C. The transceiver does not support DOM functionality.
- D. The 'transceiver_utils' library is outdated and does not support DOM data extraction.
- E. The port 'eth0' does not exist or is not correctly associated with the transceiver.

Answer: E

Explanation:
The 'Invalid port' error strongly suggests that the specified port identifier ('eth0') is either incorrect or not properly linked to the transceiver by the operating system or networking stack. While other issues like outdated libraries, lack of DOM support, or cable damage could cause problems, the specific error message points directly to a port configuration issue.

NEW QUESTION # 108
You are installing four NVIDIAAIOO GPUs into a server designed for AI training. The server motherboard has multiple PCIe Gen4 x16 slots. However, the server's power supply unit (PSU) only has three 8-pin PCIe power connectors available. What is the BEST course of action to ensure all GPUs receive adequate power?

- A. Install only three GPUs and leave the fourth unpowered.
- B. Use a PCIe power splitter cable on one of the 8-pin connectors to power two GPUs.
- C. Replace the existing PSU with a higher wattage PSU that has at least four 8-pin PCIe power connectors.
- D. Connect the GPUs using the motherboard's internal SATA power connectors.
- E. Underclock the GPUs significantly to reduce their power consumption below the available PSU capacity.

Answer: C

Explanation:
Using a splitter can overload the original connector and cause instability or damage. Leaving a GPU unpowered defeats the purpose. SATA connectors are not designed for the high power requirements of GPUs. Underclocking might work, but is not a reliable long-term solution. Replacing the PSU is the safest and most reliable solution.

NEW QUESTION # 109
You are installing eight NVIDIAAIOO GPUs in a server designed for maximum performance. The server supports NVLink. Which of the following actions will BEST improve the inter-GPU communication bandwidth?

- A. Disabling NVLink in the BIOS/UEFI settings.
- B. Only installing four of the eight GPUs to reduce the total number of connections needed.
- C. Using a standard PCIe riser card for all GPUs.
- D. Installing the GPUs in PCIe Gen3 slots instead of PCIe Gen4 slots.
- E. Ensuring the GPUs are placed in slots that support NVLink bridges and that the bridges are properly installed.

Answer: E

Explanation:
NVLink provides significantly higher bandwidth than PCIe for inter-GPIJ communication. Installing the GPUs in slots that support NVLink and properly installing the NVLink bridges will enable this faster communication. PCIe Gen4 is better than Gen3. Riser cards don't improve communication bandwidth. Disabling NVLink negates its benefits. Removing GPUs reduces overall performance.

NEW QUESTION # 110
Consider a scenario where you are setting up a high-performance computing cluster with several GPU-accelerated nodes using Slurm as the resource manager. You want to ensure that jobs requesting GPUs are only scheduled on nodes with the appropriate NVIDIA drivers and CUDA toolkit installed. How can you achieve this within Slurm?

- A. Create a custom Slurm script that checks for the presence of the NVIDIA driver and CUDA toolkit before submitting a job to a node. If the requirements are not met, the job is rejected.
- B. Use Slurm's 'GresTypeS configuration option in 'slurm.conf to define a generic resource type called 'gpu' and then configure each node to advertise the available GPIJs. Slurm will automatically ensure that jobs requesting GPUs are only scheduled on nodes with the 'gpu' resource.
- C. Utilize Slurm's Prolog and Epilog scripts to dynamically install the necessary NVIDIA drivers and CUDA toolkit on each node before and after a job runs. This ensures that the required software is always available.
- D. Install the NVIDIA Data Center GPU Manager (DCGM) on each node and configure Slurm to query DCGM for GPU availability and health. Slurm will then only schedule jobs on healthy and available GPUs.
- E. Use Slurm's node features to tag nodes with the "Feature=' keyword in 'slurm.conf. For example, tag nodes with GPUs as 'Feature=gpu' . Jobs can then request nodes with the 'gpu' feature using the option.

Answer: E

Explanation:
Using Slurm's node features is the most straightforward and recommended approach for tagging nodes with specific capabilities. The '-constraint' option allows jobs to request nodes with particular features. GresTypeS can be used, but node features provide more flexibility and control. Installing drivers dynamically is impractical and inefficient. DCGM is primarily for monitoring, not core scheduling requirements.

NEW QUESTION # 111
You're debugging performance issues in a distributed training job. 'nvidia-smi' shows consistently high GPU utilization across all nodes, but the training speed isn't increasing linearly with the number of GPUs. Network bandwidth is sufficient. What is the most likely bottleneck?

- A. Inefficient data loading and preprocessing pipeline, causing GPUs to wait for data.
- B. The global batch size has exceeded the optimal point for the model, reducing per-sample accuracy and slowing convergence.
- C. The learning rate is not adjusted appropriately for the increased batch size across multiple GPUs.
- D. NCCL is not configured optimally for the network topology, leading to high communication overhead.
- E. CUDA Graphs is not being utilized.

Answer: A,B,C,D

Explanation:
If GPUs are highly utilized but scaling is poor, the bottleneck is likely not GPU compute itself. Inefficient data pipelines mean GPUs spend time idle waiting for data. Suboptimal NCCL configurations result in communication overhead negating the benefit of more GPUs. Incorrect learning rate with larger batch size will impact covergence. Batch sizes can affect convergence and model effectiveness. While CUDA Graphs improves performance, the other answers are more pertinent to the question.

NEW QUESTION # 112
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