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NVIDIA AI Infrastructure Sample Questions (Q20-Q25):

NEW QUESTION # 20

After upgrading to HPL-AI 2.0 on a DGX A100 cluster, a 2x performance gain is observed. Which optimization is primarily responsible for this improvement?

- A. Automatic NVLink bandwidth doubling via driver updates.
- B. Doubling of GPU clock speeds through firmware updates and relevant configuration.
- C. Reduction of problem size (N) to accelerate computation.
- D. MPI-aware GPU communication that reduces CPU bottlenecks and GPU idle time.

Answer: D

Explanation:

HPL-AI (High-Performance Linpack for AI) differs from traditional HPL by utilizing lower-precision arithmetic (FP16/BF16/TF32) while maintaining FP64 accuracy through iterative refinement. The significant jump in performance seen in version 2.0 and above is largely attributed to advancements in the communication layer. In multi-node DGX clusters, the CPU often becomes a bottleneck when managing MPI (Message Passing Interface) ranks and coordinating data transfers between GPUs across the InfiniBand fabric.

By implementing MPI-aware GPU communication (and leveraging technologies like SharpV3 or NCCL integration), the benchmark significantly reduces the "GPU Idle Time" spent waiting for the CPU to orchestrate the next computation block. This optimization ensures that the GPUs remain at near 100% utilization during the intensive matrix multiplication phases. By offloading collective operations and using GPUDirect RDMA, the system bypasses the host memory and CPU, effectively doubling the effective throughput of the benchmark compared to older, CPU-heavy coordination methods.

NEW QUESTION # 21

Consider the following 'lspci' output snippet after installing an NVIDIA GPU:

```
03:00.0 VGA compatible controller: NVIDIA Corporation Device 2236 (rev a1) Subsystem: Dell Device 1234 Kernel driver in use: nvidia Kernel modules: nvidiafb, nouveau, nvidia drm, nvidia
```

What does this output indicate, and what is the POTENTIAL issue?

- A. The GPU is installed, but the 'nvidiafb' driver is causing conflicts. Solution: Remove the 'nvidiafb' module.
- B. The output indicates a hardware failure and requires a replacement of the GPU.
- C. Solution: Blacklist 'nouveau' and ensure the NVIDIA driver is correctly configured.
- D. The GPU is correctly installed, and the proprietary NVIDIA driver is loaded.
- E. The GPU is not properly installed, as indicated by 'Device 2236'. Solution: Re-seat the GPU.
- **F. The GPU is installed, but the open-source 'nouveau' driver is loaded instead of the proprietary NVIDIA driver, which could lead to suboptimal performance.**

Answer: F

Explanation:

The output shows that both the 'nvidia' driver and the 'nouveau' driver are loaded. 'nouveau' is an open-source driver, and while it allows the GPU to function, it does not provide the performance benefits of the proprietary NVIDIA driver. The presence of 'nouveau' can sometimes interfere with the proper loading of the NVIDIA driver. Blacklisting 'nouveau' ensures that only the NVIDIA driver is used.

NEW QUESTION # 22

Consider a scenario where you want to reset your NVIDIA A100 GPU back to a non-MIG mode state after having previously configured MIG. Which of the following steps are required?

- A. Run 'nvidia-smi -set-mig-mode=disable -i', then power off the system and physically remove and re-install the GPU.
- B. Run 'nvidia-smi -set-mig-mode=disable -i O', then run 'nvidia-smi -i O -migr 0', and finally reboot.
- C. Run 'nvidia-smi -destroy-mig-config -i O', then run 'nvidia-smi -set-mig-mode=disable -i O', and finally reboot.
- **D. Run 'nvidia-smi -set-mig-mode=disable -i O' and then reboot the system**
- E. Run 'nvidia-smi -set-mig-mode=disable -i O' followed by 'nvidia-smi -reset-default-mig-mode -i

Answer: D

Explanation:

To reset the GPU to non-MIG mode, the command 'nvidia-smi -set-mig-mode=disable -i' must be executed, followed by a system reboot. This ensures that the changes are applied during the next boot process. Destroying MIG config is not required to just disable the MIG mode, neither is physically reinstalling the GPU.

NEW QUESTION # 23

As the infrastructure lead for an NVIDIA AI Factory deployment, you have just uploaded the latest supported firmware packages to your DGX system. It is now critical to ensure all hardware components run the new firmware and the DGX returns to full operational capability. Which sequence best guarantees that all relevant components are correctly running updated firmware?

- **A. Initiate a cold power cycle on the system to activate firmware for components, reset the BMC using the recommended command, and perform an AC power cycle to ensure ER0T and CPLD firmware is activated.**
- B. Perform a software-driven restart on the operating system of every compute node, then use advanced tools to check firmware status, and reissue update commands if any firmware appears inactive afterward.
- C. Initiate a cold power cycle on all node trays to activate firmware, follow with a DGX reboot procedure, and use the management interface to finish activating CPLD firmware on the host.
- D. Execute a single AC power cycle on the DGX after the update process, then reset the software stack and verify status using diagnostic commands on each node for confirmation of all component updates.

Answer: A

Explanation:

Updating an NVIDIA DGX system (like the H100) is a multi-layered process because the system contains numerous programmable logic devices, including CPLDs, FPGAs, and the EROT (Electrically Resilient Root of Trust) modules. Many of these low-level hardware components cannot be updated via a simple operating system reboot. NVIDIA's official firmware update procedure requires a specific sequence to "commit" the new images to the hardware. First, the update utility (like nvfwupd) writes the images to the flash memory. To activate them, a "Cold Power Cycle" (removing and restoring power) is necessary to force the hardware to reload from the newly written flash blocks. Furthermore, because the BMC (Baseboard Management Controller) orchestrates the power-on sequence and monitors the EROT, it must be reset (Option D) to synchronize its state with the new component versions. Finally, an "AC Power Cycle" ensures that even the standby-power components, such as the power delivery controllers and CPLDs, undergo a full hardware reset.

Skipping these steps can result in "Incomplete" or "Mismatched" firmware versions, where the OS reports one version while the hardware continues to run old, potentially buggy code in the background.

NEW QUESTION # 24

Which of the following are key benefits of using NVIDIA NVLink Switch in a multi-GPU server setup for AI and deep learning workloads?

- A. Enhanced security features compared to PCIe based interconnections.
- **B. Reduced latency in inter-GPU data transfers.**
- C. Simplified GPU resource management.
- **D. Support for larger GPU memory pools than a single server can physically accommodate.**
- **E. Increased GPU-to-GPU communication bandwidth.**

Answer: B,D,E

Explanation:

NVLink provides significantly higher bandwidth and lower latency compared to PCIe, enabling faster communication between GPUs. NVLink switches allow for pooling of GPU memory across multiple servers, enabling training of larger models that wouldn't fit on a single server's GPU memory. While it enhances infrastructure capabilities, it doesn't inherently simplify GPU resource management or directly provide enhanced security compared to PCIe regarding the data transfer. Management features might exist on top of the NVLink infrastructure.

NEW QUESTION # 25

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