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NVIDIA NCP-AII NVIDIA AI Infrastructure

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understanding of the exam, therefore you can pass the exam more easily.

NVIDIA AI Infrastructure Sample Questions (Q182-Q187):

NEW QUESTION # 182

You are configuring a server with NVIDIA GPUs for optimal power efficiency. You want to leverage NVIDIA's power management features to minimize energy consumption during idle periods. Which of the following actions would be the MOST effective in achieving this goal, without significantly impacting performance during active workloads?

- A. Reduce the GPU's clock speeds to the lowest possible setting, regardless of workload.
- B. Remove one or more GPUs from the server to reduce overall power consumption.
- C. Enable NVIDIA's Adaptive Clocking and Power Limiting features, allowing the GPU to dynamically adjust its clock speeds and power consumption based on the workload.
- D. Disable all GPU power management features to ensure maximum performance at all times.
- E. Set a very low static power limit for the GPUs, significantly restricting their performance even during active workloads.

Answer: C

Explanation:

Enabling NVIDIA's Adaptive Clocking and Power Limiting features is the MOST effective approach. These features allow the GPU to dynamically adjust its clock speeds and power consumption based on the workload, minimizing energy consumption during idle periods while maximizing performance during active workloads. Setting a fixed low clock speed (A) or power limit (E) would severely impact performance. Disabling power management (C) wastes energy. Removing GPUs (D) reduces performance capacity.

NEW QUESTION # 183

Your AI training pipeline involves a pre-processing step that reads data from a large HDF5 file. You notice significant delays during this step. You suspect the HDF5 file structure might be contributing to the slow read times. What optimization technique is MOST likely to improve read performance from this HDF5 file?

- A. Reorganizing the HDF5 file to improve data contiguity and chunking.
- B. Encrypting the HDF5 file for enhanced security.
- C. Storing the HDF5 file on a network file system like NFS.
- D. Converting the HDF5 file to a CSV file.
- E. Compressing the HDF5 file using gzip.

Answer: A

Explanation:

Reorganizing the HDF5 file (option C) to improve data contiguity and chunking is the most effective optimization. HDF5 performance is highly dependent on how the data is laid out within the file. Contiguous data and optimal chunk sizes allow for more efficient I/O operations. Converting to CSV (A) loses the hierarchical structure of HDF5. Storing on NFS (B) adds network overhead. Compression (D) can reduce storage space but increases decompression overhead. Encryption (E) adds overhead without improving read performance.

NEW QUESTION # 184

You're deploying a BlueField-2 DPU in a cloud environment and need to ensure the integrity of the DPU's firmware. You want to verify that the firmware hasn't been tampered with. Which of the following methods provides the strongest level of assurance for firmware integrity?

- A. Verifying the SHA256 checksum of the firmware image against a known good value provided by NVIDIA.
- B. Comparing the firmware version reported by the DPU with the version listed in the NVIDIA release notes.
- C. Checking the MD5 checksum of the firmware image against a known good value.
- D. Checking the file size of the firmware image against a known good value.
- E. Using a digitally signed firmware image and verifying the signature using NVIDIA's public key.

Answer: E

Explanation:

Digitally signed firmware provides the strongest guarantee of integrity. The signature verifies that the firmware hasn't been tampered

with since it was signed by NVIDIA. SHA256 checksums are good, but digital signatures are cryptographically stronger. MD5 checksums are considered weak and easily compromised. Firmware version and file size offer minimal assurance against sophisticated attacks.

NEW QUESTION # 185

Which of the following is the MOST important reason for using a dedicated storage network (e.g., InfiniBand or RoCE) for AI/ML workloads compared to using the existing Ethernet network?

- A. Improved security due to network isolation.
- B. Automatic Quality of Service (QoS) prioritization for AI/ML traffic.
- C. Simplified network management and configuration.
- **D. Lower latency and higher bandwidth for data transfer.**
- E. Reduced cost compared to upgrading the existing Ethernet infrastructure.

Answer: D

Explanation:

The primary benefit of a dedicated storage network like InfiniBand or RoCE is the significant improvement in latency and bandwidth (option B) compared to Ethernet. These technologies are designed for high-performance computing and can handle the intense I/O demands of AI/ML workloads. While security (A) can be improved through isolation, and QoS (E) is possible, the performance advantage is the most crucial factor. Cost (D) is generally higher, and management (C) can be more complex.

NEW QUESTION # 186

You're optimizing an Intel Xeon server with 4 NVIDIA A100 GPUs for a computer vision application that uses CUDA. You notice that the GPU utilization is fluctuating significantly, and performance is inconsistent. Using 'nvprof', you identify that there are frequent stalls in the CUDA kernels due to thread divergence. What are possible causes and solutions?

- A. The CUDA compiler is generating suboptimal code. Try using different compiler optimization flags (e.g., '-O3' or '-ftz=true').
- **B. The input data is not properly aligned in memory. Ensure that data is aligned to 128-byte boundaries using aligned memory allocation techniques.**
- C. The GPUs are overheating, causing thermal throttling. Improve the server's cooling.
- **D. The CUDA code contains conditional branches that lead to different execution paths for different threads within the same warp. Rewrite the CUDA code to minimize branching and favor uniform execution paths within warps.**
- E. The CUDA driver version is incompatible with the CUDA toolkit version. Update the CUDA driver to a compatible version.

Answer: B,D

Explanation:

Thread divergence occurs when threads within the same warp take different execution paths due to conditional branches, leading to serialization. Re-writing code to minimize branching is critical. Memory alignment ensures that threads access memory efficiently and doesn't cause thread divergence stalls, which is often misaligned. Thermal throttling is a possible cause of fluctuating utilization but doesn't directly explain the stalls identified in the profiler. Compiler flags and driver version issues can cause performance problems but are less likely to cause frequent thread divergence stalls.

NEW QUESTION # 187

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