2026 Pure Storage FlashArray-Implementation-Specialist: Pure Storage Certified FlashArray Implementation Specialist–High Pass-Rate New Test Labs

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Pure Storage FlashArray-Implementation-Specialist Exam Syllabus Topics:

Topic	Details
Topic 1	• Post-Installation • Upgrade: This section of the exam measures the skills of FlashArray Implementation Specialists and evaluates how professionals confirm system functionality after installation or an upgrade. It involves validating connectivity, running health checks, confirming configurations, and ensuring that the deployment meets operational expectations.
Topic 2	• Upgrades: This section of the exam measures the skills of FlashArray Implementation Specialists and focuses on tasks involved in managing firmware and software upgrades. Candidates must demonstrate knowledge of upgrade planning, verification steps, and rollback procedures, ensuring that systems are updated with minimal disruption to service.
Topic 3	• Pre-Installation • Upgrade: This section of the exam measures the skills of Enterprise Infrastructure Technicians and covers all preparation activities before deploying or upgrading a Pure Storage FlashArray. It includes understanding environmental requirements, verifying prerequisites, checking compatibility, and validating system readiness through appropriate tools and documentation.
Topic 4	• Installation: This section of the exam measures the skills of Enterprise Infrastructure Technicians and focuses on executing a successful installation of FlashArray systems. It tests the ability to perform physical setup, cabling, configuration of network settings, and the application of initial system configurations necessary for full deployment.

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Pure Storage Certified FlashArray Implementation Specialist Sample Questions (Q56-Q61):

NEW QUESTION # 56
When installing the first DirectFlash Shelf on a FlashArray//XR2/3, what mezzanine ports are used on the controllers?

- A. ETH7 and ETH9
- B. ETH6 and ETH8
- C. ETH6 and ETH9

Answer: C

Explanation:
When installing the first DirectFlash Shelf (DFS) on a FlashArray//XR2 or //XR3 utilizing the onboard or default mezzanine connectivity, the correct ports to use are ETH6 and ETH9.
Hardware Layout: The FlashArray//XR2 and //XR3 utilize a specific Ethernet Mezzanine (EMEZZ) slot for backend connectivity to NVMe-oF shelves. This card typically enumerates its ports as ETH6 through ETH9 (a 4-port configuration).
Cabling Rule: To maximize physical separation and ensure redundancy across the internal ASIC lanes of the mezzanine card, standard cabling procedures for the first shelf dictate using the outer ports: ETH6 (Port 0) and ETH9 (Port 3).
Redundancy: This configuration (Port 0 and Port 3) is preferred over using adjacent ports (like 6 and 7) to reduce the risk of a single lane cluster failure affecting both connections to the shelf. The second shelf, if added later, would typically utilize the remaining ports (ETH7 and ETH8).

NEW QUESTION # 57
FlashArray//C and //E models use which flash storage architecture, identifiable by gray tabs on the DirectFlash Module carriers?

- A. QLC flash
- B. SLC Flash
- C. TLC Flash

Answer: A

Explanation:
Pure Storage differentiates its product lines based on the type of NAND flash used, optimizing for either performance or capacity/cost.
* FlashArray//X uses TLC (Triple-Level Cell) flash for high performance and endurance. These modules typically have orange tabs.
* FlashArray//C and FlashArray//E are designed for high-capacity, capacity-optimized workloads.
They utilize QLC (Quad-Level Cell) flash.
QLC flash stores 4 bits per cell, offering higher density at a lower cost per terabyte, but with different endurance characteristics managed by the DirectFlash software. To help engineers and customers physically distinguish these modules, QLC DirectFlash Modules feature gray release tabs on the carrier. Identifying these tabs confirms that the correct media type is being installed into the capacity-oriented //C or //E chassis.
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NEW QUESTION # 58
What command will view NVRAM install and update status?

- A. purearray list
- B. puredrive list
- C. purehw list --all

Answer: C

Explanation:
The command purehw list --all is the correct CLI instruction to view the comprehensive status of all hardware components, including the NVRAM modules.
Command Scope: While purehw list provides a general hardware summary, the --all flag (or specifying --type nvram) ensures that granular details about the NVRAM modules-such as their presence, health status, and firmware/update status-are displayed.
NVRAM Context: In FlashArray models that utilize dedicated NVRAM modules (like the FA-400, //M, or specific //X configurations), these components are critical for write acknowledgement. During an upgrade or installation, verification that these modules are "healthy" and "idle" (not destaging) is mandatory.
Incorrect Options:
purearray list (Option A) displays high-level array attributes like the array name, ID, and overall OS version, but not component-level hardware status.
puredrive list (Option B) is specifically for managing the storage media (SSDs/DirectFlash Modules) and does not report on the non-volatile RAM cache modules housed in the controller or chassis.

NEW QUESTION # 59
When transforming an array from SAS to NVMe with the Evergreen XFORM upgrade, when should a swing shelf be installed?

- A. When the array is half populated with Flash Modules
- B. When the chassis is populated with two data packs
- C. When there is insufficient free space to evac capacity.

Answer: C

Explanation:
A swing shelf (temporary external capacity) is required during a SAS-to-NVMe (Evergreen) upgrade specifically when there is insufficient free space elsewhere in the array to evacuate the data residing in the legacy chassis.
The Evergreen "Stateless" or XFORM upgrade involves replacing the legacy SAS-based controller chassis (which contains SAS SSDs) with a new NVMe-based FlashArray//X chassis (which uses DirectFlash Modules).
Evacuation Requirement: Before the old chassis can be removed, the data on its internal drives must be moved to a safe location. If the customer already has external SAS expansion shelves with enough free space to hold this data, Purity will transparently migrate the data there, and no swing shelf is needed.
Swing Shelf Scenario: If the array is highly utilized (e.g., the chassis is full and external shelves are full or non-existent), the Implementation Engineer must attach a temporary "swing shelf" provided by Pure Storage. This shelf acts as the evacuation target. Once the data is moved to the swing shelf, the old chassis is replaced. The data is then migrated back from the swing shelf to the new NVMe media in the new chassis, and the swing shelf is returned.

NEW QUESTION # 60
Which cards are required to connect a DirectFlash Shelf to an FlashArray//XLR5?

- A. 2-port 200GbE Ethernet/RoCE
- B. 2-Port 50GbE
- C. 2-Port 100GbE iSCSI/RoCE

Answer: C

Explanation:
The FlashArray//XL series (specifically the //XLR5 in this context) utilizes a 100 Gigabit Ethernet-based back-end fabric to communicate with DirectFlash Shelves (DFS). This connection relies on the RoCE (RDMA over Converged Ethernet) protocol to ensure low-latency access to the external NVMe modules.

The specific PCIe interface card validated for this connectivity is often labeled in the Bill of Materials (BOM) or configuration guides as the 2-Port 100GbE iSCSI/RoCE card (or simply the 100GbE Ethernet/RoCE card depending on the specific firmware/labeling revision). Crucially, it must be a 100GbE capability card.
The //XL platform standardized on 100GbE for its shelf interconnects, moving away from the 50GbE used in some previous generations of the //X series. The 200GbE cards are typically reserved for high-speed host connectivity or clustering, not the standard shelf loop. Therefore, ensuring the BOM includes the correct 100GbE RoCE-capable cards is essential for successful shelf detection and operation during installation.

NEW QUESTION # 61
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