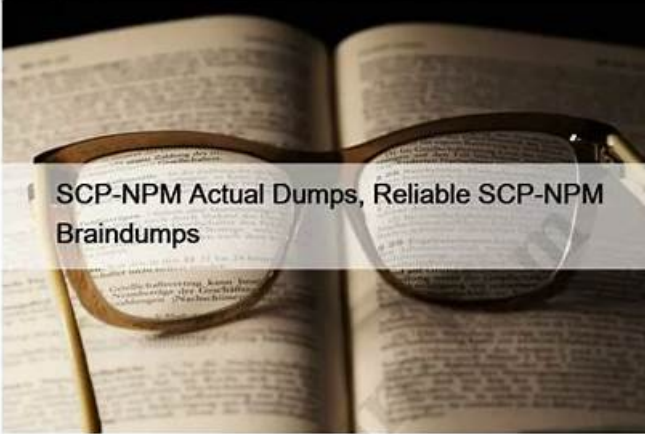


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SolarWinds SCP-NPM SolarWinds Network Performance Monitor (NPM) Exam 1



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EMC Dell PowerEdge Foundations v2 Exam Sample Questions (Q38-Q43):

NEW QUESTION # 38

An IT manager is looking at the Dell PowerEdge R740xd model. What does the "xd" in the model's name indicate?

- A. Indicates extra drives
- B. Indicates tower form factor
- C. Indicates modular design
- D. Indicates high density

Answer: A

Explanation:

Comprehensive and Detailed Explanation From Exact Extracts: The "xd" in the PowerEdge R740xd model name indicates extra drives, signifying a configuration with enhanced storage capacity, supporting more drives or larger storage options for data-intensive workloads. This distinguishes it from standard R740 models. Modular design and high density apply to other models, and "xd" does not denote a tower form factor (R-series are racks). This naming convention aids in selecting appropriate PowerEdge models. Exact extract:

"Explain how different PowerEdge server models (e.g., rack servers, tower servers, blade servers) are better suited for specific roles." Reference: Dell PowerEdge Foundations v2 Exam Description (D-PE-FN-01), Topic: Introduction to Servers (28%).

NEW QUESTION # 39

Power supply unit 2 (PSU 2) on a Dell PowerEdge R760 server has been losing power several times a day.

The system administrator is planning to replace the power supply but is unsure which power supply is referenced as PSU 2. What is the most reliable way for the system administrator to visually identify PSU 2?

- A. Check the chassis label near the power supplies for PSU numbers
- B. Look for a constant amber LED on PSU 1, indicating a problem with that unit
- C. Assume PSU 2's LEDs are off because the iDRAC has disabled it
- D. Use the iDRAC web UI to activate the blink option for PSU 2's LED

Answer: A

Explanation:

The most reliable way to identify PSU 2 on a PowerEdge R760 is to check the chassis label near the power supplies, which clearly marks PSU numbers for accurate identification. This physical label ensures certainty without assumptions. Using iDRAC to blink LEDs is useful but less direct, assuming LEDs are off is unreliable, and checking PSU 1's LED does not identify PSU 2. Exact extract: "Describe maintenance functions, shutdowns, reboots of a PowerEdge Server... Explain the power distribution considerations." Reference: Dell PowerEdge Foundations v2 Exam Description (D-PE-FN-01), Topic: Maintenance (18%).

NEW QUESTION # 40

A data center is designed with cold aisle containment. How are cold aisles used in this design to manage server temperatures?

- A. Isolate the cold aisle to prevent any airflow from the hot aisle.
- B. Direct hot exhaust air from the servers into the cold aisle.
- C. Recirculate hot air within the cold aisle to preheat intake air.
- D. Pump cold air into the cold aisle for server intake.

Answer: D

Explanation:

In cold aisle containment designs, cold aisles are used to supply chilled air directly to server intakes by pumping cold air from air handlers or CRAC units into the enclosed cold aisle, ensuring efficient cooling and preventing mixing with hot exhaust air. This isolation improves energy efficiency and maintains optimal server temperatures. Directing hot air into the cold aisle or recirculating it would increase temperatures, defeating the purpose. Containment focuses on separating airflows for better thermal management in PowerEdge server deployments. Exact extract: "Explain the cooling and thermal considerations in PowerEdge Servers (Direct Liquid

Cooling, Immersion Cooling, Air Cooling)... Describe maintenance functions, shutdowns, reboots of a PowerEdge Server." Reference: Dell PowerEdge Foundations v2 Exam Description (D-PE-FN-01), Topic: Maintenance (18%).

NEW QUESTION # 41

In ECC memory, how many extra bits are used for every 64-bit value to facilitate single-bit error correction, based on Hamming code?

- A. ECC adds 32 error correction bits per 64-bit data segment.
- B. ECC uses 4 extra bits for every 64-bit value for correction.
- **C. ECC employs 8 additional bits for every 64-bit value.**
- D. ECC uses 16 parity bits for every 64-bit data value.

Answer: C

Explanation:

Error Correcting Code (ECC) memory uses 8 additional bits for every 64-bit data value to detect and correct single-bit errors using Hamming code, resulting in 72-bit modules. This enhances data integrity in servers by repairing errors without interruption. Fewer bits (4) would only detect, not correct; more (16 or 32) are unnecessary for standard single-bit correction. ECC is beneficial in PowerEdge servers for reliable operations.

Exact extract: "Why is Error Correcting Code (ECC) memory beneficial in Dell EMC PowerEdge servers? D.

Can repair errors without interrupting other server operations... Describe the Memory and Expansion cards." Reference: Dell PowerEdge Foundations v2 Exam Description (D-PE-FN-01), Topic: Introduction to Servers (28%) and Maintenance (18%).

NEW QUESTION # 42

A server has a PCIe 5.0 x1 lane. What is the theoretical bandwidth?

- A. 0.98 GB/s
- **B. 3.94 GB/s**
- C. 7.88 GB/s
- D. 1.97 GB/s

Answer: B

Explanation:

PCIe 5.0 offers a per-lane bandwidth of approximately 3.94 GB/s for an x1 lane. This is calculated based on PCIe 5.0's data rate of 32 GT/s, with each lane providing roughly 32 Gbps (4 GB/s) after accounting for encoding overhead (128b/130b). For an x1 lane, this results in 3.94 GB/s, making it the correct answer for PowerEdge server configurations leveraging PCIe 5.0 for high-speed connectivity. Other options reflect incorrect calculations or older PCIe versions. Exact extract: "Describe the Memory and Expansion cards...

Compare the on-board network on a PowerEdge Server." Reference: Dell PowerEdge Foundations v2 Exam Description (D-PE-FN-01), Topic: Server Architecture and Roles (22%).

NEW QUESTION # 43

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