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Pure Storage FlashArray-Implementation-Specialist Exam Syllabus Topics:

Topic	Details
Topic 1	<ul style="list-style-type: none">• Installation: This section of the exam measures the skills of Enterprise Infrastructure Technicians and focuses on executing a successful installation of FlashArray systems. It tests the ability to perform physical setup, cabling, configuration of network settings, and the application of initial system configurations necessary for full deployment.

Topic 2	<ul style="list-style-type: none"> • Upgrades: This section of the exam measures the skills of FlashArray Implementation Specialists and focuses on tasks involved in managing firmware and software upgrades. Candidates must demonstrate knowledge of upgrade planning, verification steps, and rollback procedures, ensuring that systems are updated with minimal disruption to service.
Topic 3	<ul style="list-style-type: none"> • Post-Installation • Upgrade: This section of the exam measures the skills of FlashArray Implementation Specialists and evaluates how professionals confirm system functionality after installation or an upgrade. It involves validating connectivity, running health checks, confirming configurations, and ensuring that the deployment meets operational expectations.
Topic 4	<ul style="list-style-type: none"> • Pre-Installation • Upgrade: This section of the exam measures the skills of Enterprise Infrastructure Technicians and covers all preparation activities before deploying or upgrading a Pure Storage FlashArray. It includes understanding environmental requirements, verifying prerequisites, checking compatibility, and validating system readiness through appropriate tools and documentation.

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Pure Storage Certified FlashArray Implementation Specialist Sample Questions (Q102-Q107):

NEW QUESTION # 102

An Implementation Engineer is performing an inventory for an NDU from a FlashArray//X70R2 to a FlashArray//X70R4 and discovers that a complete FlashArray//X70R4 was shipped instead of an NDU kit. What is the minimum hardware the Implementation Engineer should use from the new array to complete the NDU?

- A. Controllers and power supplies
- **B. Controllers**
- C. Controllers and NVRAM

Answer: B

Explanation:

In a Non-Disruptive Upgrade (NDU) scenario, the goal is to replace the compute and control logic of the array while preserving the existing data and minimizing hardware waste. When upgrading from a FlashArray//X70R2 to an //X70R4, the primary components that change are the Controllers themselves. The chassis, power supplies, and NVRAM modules (depending on the specific generation jump and health) can often be reused or are not the primary target of the replacement in a standard NDU kit.

If a complete array is shipped by mistake (often called a "greenfield" or "net-new" array shipment) instead of the specific upgrade kit, the engineer should only extract and use the Controllers from the new shipment. The existing chassis and its passive components (like the midplane and power supplies) are already racked, cabled, and functioning. Swapping the chassis or power supplies would turn the operation into a disruptive migration or a much more complex "chassis replacement" procedure, which is unnecessary for a controller upgrade.

Therefore, the engineer should unpack the new controllers, perform the NDU procedure to replace the old controllers one by one, and leave the rest of the new hardware (chassis, empty slots, etc.) as spare or return it. This approach adheres to the NDU procedure of swapping the "brains" of the storage while keeping the "body" (chassis and drives) intact.

NEW QUESTION # 103

Before installing array rails, where should the cage nuts be placed?

- A. Lowest square of lowest RU
- **B. Top square of the lowest RU**
- C. Lowest square of top RU

Answer: B

Explanation:

When installing rails for a FlashArray in a rack requiring cage nuts (square hole racks that are not using the tool-less mechanism, or specific threaded adaptations), the correct placement for the cage nut is the top square of the lowest Rack Unit (RU) that the rail will occupy.

Pure Storage rail kits typically secure at the bottom of the targeted rack unit.

* The "3-Hole" Rule: A standard Rack Unit (1U) consists of three vertical holes (often referred to as the bottom, middle, and top hole).

* Alignment: The rail flange is designed to align such that the retaining screw or positioning stud engages specific holes to support the weight. For the standard securement point, the documentation specifies installing the cage nut in the top hole of the specific RU where the rail base sits (often the

"lowest" RU if the device spans multiple U's, though the rail itself is 1U high).

* Precision: Placing it in the "Lowest square of the lowest RU" (Option A) would interfere with the rail's shelf lip or the device below it. Correct alignment ensures the rail is level and the chassis slides in without binding.

NEW QUESTION # 104

How many chassis capacity drive slots come standard on a FlashArray//X Gen 2 chassis?

- A. 28 slots
- **B. 20 slots**
- C. 24 slots
- D. 40 slots

Answer: B

Explanation:

Understanding the physical chassis layout of the Pure Storage FlashArray is essential for capacity planning and hardware installation. The FlashArray//X Gen 2 (and Gen 3) utilizes a highly efficient 3U chassis design.

When looking at the front of a standard //X chassis, there are a total of 24 physical module bays. However, these bays serve fundamentally different architectural purposes:

* Capacity Drive Slots: There are exactly 20 slots strictly dedicated to capacity drives (DirectFlash Modules, or DFMs). These are numbered 0 through 19 and are where the data packs are installed to provide raw storage capacity.

* NVRAM Slots: The remaining 4 slots, located centrally in the chassis, are dedicated exclusively to NVRAM modules (NVB0, NVB1, NVB2, and NVB3). These provide the highly available, non-volatile write cache for the array and cannot be used for capacity expansion.

Therefore, when an Implementation Engineer is assessing the chassis for data pack expansion or module upgrades, there are exactly 20 standard capacity drive slots available before an external DirectFlash Shelf (DFS) must be added. (Note: FlashArray//XL utilizes a larger 5U chassis with 40 capacity slots, but the standard //X remains at 20).

NEW QUESTION # 105

A FlashArray//XR2/3 has an Ethernet mezzanine (EMEZ), what are the interface names of the ports on the mezzanine?

- A. ETH10, ETH11, ETH12, and ETH13
- **B. ETH6, ETH7, ETH8, and ETH9**
- C. ETH8, ETH9, ETH10, and ETH11

Answer: B

Explanation:

On a FlashArray//XR2 or //XR3 controller equipped with an optional Ethernet Mezzanine (EMEZ) card, the additional ports are

enumerated as ETH6, ETH7, ETH8, and ETH9.

The port numbering logic on these controllers follows a strict sequence:

ETH0 and ETH1: Fixed onboard 1GbE Management ports.

ETH2 and ETH3: Fixed onboard 10/25GbE ports (often used for replication or iSCSI).

ETH4 and ETH5: Reserved or used by the first PCIe slot expansion (if populated with Ethernet).

ETH6 through ETH9: Assigned to the Ethernet Mezzanine (EMEZZ) slot.

The EMEZZ is a specific internal slot distinct from the standard PCIe risers. When populated, the Purity operating system reserves the eth6-eth9 block for these interfaces. Options B and C are incorrect because they describe port ranges that would typically be assigned to PCIe expansion cards in Slot 1 or Slot 2 (e.g., eth10+), or they simply do not align with the hardcoded enumeration logic of the R2/R3 controller architecture. Correct identification of these interfaces is critical for configuring link aggregation (LACP) or assigning iSCSI IPs during the initial setup.

NEW QUESTION # 106

What slot does a PCIe SAS card need to be installed in a FlashArray//XR2/3?

- A. 0
- **B. 1**
- C. 2

Answer: B

Explanation:

On a FlashArray//XR2 or //XR3, if a dedicated PCIe SAS HBA (Host Bus Adapter) is required for external shelf connectivity (expanding beyond the onboard SAS ports), it must be installed in Slot 1.

The PCIe slot prioritization on the FlashArray//XR2/R3 controllers is strictly defined to ensure optimal performance and lane distribution:

* Slot 0: Reserved primarily for frontend Host I/O cards (Fibre Channel or iSCSI/Ethernet). This slot usually connects to CPU 0 and offers high bandwidth for host traffic.

* Slot 1: Designated as the primary expansion slot for backend connectivity (SAS cards for expansion shelves) or secondary Host I/O if no SAS expansion is needed.

* Slot 2: On smaller models (X10/X20), this is often the only host I/O slot, but on the larger X50/X70/X90 chassis, it is used for additional Host I/O.

If an Implementation Engineer installs a SAS card in Slot 0, the system may fail to recognize the shelves correctly or may flag a configuration error, as the Purity software expects backend expansion traffic to route through the bus associated with Slot 1. Therefore, for SAS expansion cards, Slot 1 is the mandatory location.

NEW QUESTION # 107

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