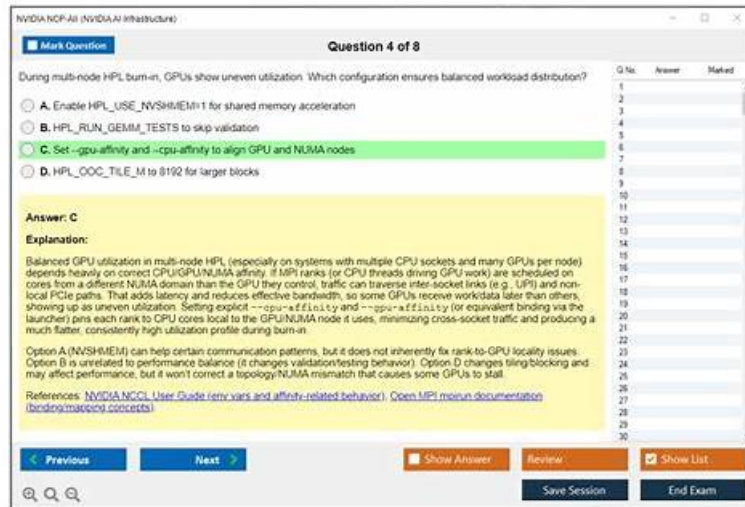


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The NVIDIA NCP-AII Certification Exam is one of the valuable credentials that are designed to prove an NVIDIA aspirant's technical expertise. With the NVIDIA AI Infrastructure (NCP-AII) certificate they can be competitive and updated in the highly competitive market. The NVIDIA Certification Questions offers a great opportunity for beginners and experienced professionals to not only validate their skills but also advance their careers.

NVIDIA NCP-AII Exam Syllabus Topics:

Topic	Details
Topic 1	• System and Server Bring-up: Covers end-to-end physical setup of GPU-based AI infrastructure, including BMC • OOB • TPM configuration, firmware upgrades, hardware installation, and power and cooling validation to ensure servers are workload-ready.
Topic 2	• Cluster Test and Verification: Covers full cluster validation through HPL and NCCL benchmarks, NVLink and fabric bandwidth tests, cable and firmware checks, and burn-in testing using HPL, NCCL, and NeMo.
Topic 3	• Physical Layer Management: Covers configuring BlueField network platform devices and setting up Multi-Instance GPU (MIG) partitioning for AI and HPC workloads.
Topic 4	• Control Plane Installation and Configuration: Covers deploying the software stack including Base Command Manager, OS, Slurm • Enroot • Pyxis, NVIDIA GPU and DOCA drivers, container toolkit, and NGC CLI.
Topic 5	• Troubleshoot and Optimize: Covers identifying and replacing faulty hardware components such as GPUs, network cards, and power supplies, along with performance optimization for AMD • Intel servers and storage.

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NVIDIA AI Infrastructure Sample Questions (Q128-Q133):

NEW QUESTION # 128

In a data center utilizing NVIDIA GPUs and NVLink, what is the primary advantage of using a direct-attached NVLink network topology compared to routing traffic over the network?

- A. Higher bandwidth and lower latency between GPUs
- B. Improved power efficiency
- C. Simplified network configuration
- D. Increased network security
- E. Reduced cost of network infrastructure

Answer: A

Explanation:

Direct-attached NVLink provides significantly higher bandwidth and lower latency compared to routing traffic over a traditional network. This is crucial for applications that require intensive GPU-to-GPU communication, such as large-scale AI training. While direct-attached NVLink can simplify configuration in some cases, its primary advantage is the performance improvement.

NEW QUESTION # 129

You are leading a project to enhance the energy efficiency of a data center that heavily relies on AI workloads. NVIDIA suggests moving beyond traditional metrics like Power Usage Effectiveness (PUE) to better capture the efficiency of modern data centers. Which strategy should you prioritize?

- A. Use watts used as the primary measure of efficiency, as it accurately reflects the power input at any given time.
- B. Use Power Usage Effectiveness as the primary metric while supplementing it with additional measures of useful work done per unit of energy.
- C. Focus on integrating kilowatt-hours into existing metrics to better reflect the actual energy used for productive work.
- D. Develop benchmarks tailored to specific workloads, such as MLPerf for AI applications, to better understand energy use in real-world scenarios.

Answer: D

Explanation:

Traditional data center metrics like PUE (Power Usage Effectiveness) only measure how much energy is "wasted" by cooling and power delivery relative to the IT load; they say nothing about how efficiently that IT load is performing its task. In an AI Factory, "Efficiency" is better defined by the amount of AI training or inference performed per watt. NVIDIA advocates for the use of workload-specific benchmarks, such as MLPerf, to quantify this. MLPerf measures the time and energy required to complete standardized AI tasks (like training a ResNet-50 model or an LLM). By prioritizing these benchmarks (Option C), an organization can compare the energy efficiency of different hardware architectures (e.g., A100 vs. H100) or different software optimizations (e.g., FP8 vs. FP16). For example, even if an H100 system draws more peak power than an older system, its ability to complete a training job 9x faster results in a significantly lower "Total Energy Consumed per Job". This shift from "infrastructure efficiency" (PUE) to "computing efficiency" (MLPerf-per-watt) is essential for modern AI data centers aiming for sustainability and cost-effective scaling.

NEW QUESTION # 130

You're designing a data center network for inference workloads. The primary requirement is high availability. Which of the following considerations are MOST important for your topology design?

- A. Implementing redundant paths

- B. Prioritizing north-south bandwidth over east-west bandwidth
- C. Using the cheapest possible switches
- **D. Minimizing hop count**
- E. Centralized routing

Answer: A,D

Explanation:

High availability necessitates redundant paths to avoid single points of failure. Minimizing hop count reduces latency and the potential for network congestion. Using cheap switches is detrimental to availability. Inference workloads typically involve both north-south and east-west traffic, but redundant paths and minimizing hop count are most crucial for availability. Centralized routing can be a single point of failure and is generally less resilient than distributed routing.

NEW QUESTION # 131

You are training a deep neural network using NCCL to coordinate communication across four GPUs in a single node. During early performance testing, you notice inconsistent scaling and longer-than-expected training times, even though all GPUs are being used. Which strategy would most effectively improve NCCL efficiency and collective operation performance in this setting?

- A. Disable automatic load balancing so that the deep learning framework can dynamically assign samples to any GPU available during each iteration.
- **B. Adjust the batch size so that each GPU receives an equal-sized portion of the batch, ensuring all GPUs process similar workloads and communication is evenly distributed.**
- C. Assign the largest possible workload to the first GPU to maximize its utilization, and allow the remaining GPUs to process smaller or variable batch sizes as needed.
- D. Increase the communication frequency between GPUs while allowing workloads to be unevenly split, so synchronization is more frequent and model updates happen faster.

Answer: B

Explanation:

The best strategy is to divide the batch evenly so each GPU receives a similar amount of work. NCCL collective operations, such as all-reduce, synchronize data across all participating GPUs. If one GPU receives more work than the others, the remaining GPUs may finish early and wait at synchronization points, creating poor scaling and longer training iterations. Balanced per-GPU batch sizes reduce straggler effects and allow computation and communication to proceed more predictably. Assigning the largest workload to one GPU creates imbalance and worsens synchronization delays. Disabling load balancing or allowing dynamic uneven assignment does not guarantee consistent work distribution. Increasing communication frequency with uneven workloads can increase overhead and make NCCL synchronization even less efficient. In a single-node DGX or HGX system, NCCL can take advantage of NVLink, NVSwitch, and PCIe topology, but application-level balance is still required. Proper batch partitioning, rank placement, GPU affinity, and framework configuration all help ensure that NCCL collectives complete efficiently and that all GPUs contribute evenly to each training step.

NEW QUESTION # 132

You're optimizing an Intel Xeon server with 4 NVIDIA GPUs for inference serving using Triton Inference Server. You've deployed multiple models concurrently. You observe that the overall throughput is lower than expected, and the GPU utilization is not consistently high.

What are potential bottlenecks and optimization strategies? (Select all that apply)

- A. Insufficient PCIe bandwidth between CPU and GPUs. Reconfigure PCIe lanes to improve bandwidth allocation to each GPU.
- **B. Model loading and unloading overhead. Use model ensemble or dynamic batching to reduce frequency.**
- **C. The models are memory-bound. Reduce the model precision (e.g., FP32 to FP16 or INT8).**
- **D. The GPUs are underutilized due to small batch sizes. Implement dynamic batching to increase batch sizes.**
- **E. Insufficient CPU cores to handle the model loading and preprocessing requests. Increase the number of Triton instance groups for CPU-based models.**

Answer: B,C,D,E

Explanation:

Multiple factors can contribute to low throughput in inference serving. Model loading overhead is significant, and dynamic batching is

crucial to maximize throughput. Insufficient CPU cores and memory constraints on the GPU also limit performance. Model precision reduction helps reduce memory footprint and increase throughput. While PCIe bandwidth is a factor, it is often not the primary bottleneck in inference serving.

NEW QUESTION # 133

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