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NVIDIA AI Infrastructure Sample Questions (Q298-Q303):

NEW QUESTION # 298
An NVIDIA DGX server with 8 GPUs is experiencing performance issues during a distributed deep learning training run. You suspect a problem with the GPU interconnects. You have already confirmed that NVLink is active. What is the most thorough approach to diagnose potential bandwidth or latency bottlenecks in the GPU-to-GPU communication paths?

- A. Use 'nvidia-smi topo -m' to visualize the GPU topology and check the reported link speeds. Any links with significantly lower speeds are suspect.
- B. Examine the output of 'dmesg' for any NVLink-related error messages or warnings.
- C. Run NCCL all-reduce benchmarks (e.g., using the NCCL tests) to measure the actual communication bandwidth between all pairs of GPUs. Compare the results to expected theoretical peak bandwidth.
- D. All of the above
- E. Monitor GPU utilization with 'nvidia-smi' during the training run. Uneven utilization across GPUs indicates a potential

communication bottleneck.

Answer: D

Explanation:
Explanation:Diagnosing GPU interconnect bottlenecks requires a multi-faceted approach. Visualizing the topology with 'nvidia-smi topo -m' , running NCCL benchmarks, monitoring GPU utilization for imbalances, and checking system logs for errors are all necessary for identifying and resolving communication issues. Ignoring any of these steps could lead to incomplete diagnosis and suboptimal performance.

NEW QUESTION # 299
An AI infrastructure utilizes NVIDIA ConnectX-7 NICs for inter-node communication. The requirement is to achieve a bandwidth of 400GbE with low latency over a distance of 100 meters. Which transceiver and cable type combination is MOST suitable for this scenario?

- A. QSFP-DD DR4 transceiver with single-mode fiber cable.
- B. QSFP-DD FR4 transceiver with single-mode fiber cable.
- C. QSFP-DD SR4 transceiver with OM4 multi-mode fiber cable.
- D. QSFP-DD LR8 transceiver with OM4 multi-mode fiber cable.
- E. QSFP-DD SR8 transceiver with OM3 multi-mode fiber cable.

Answer: C

Explanation:
QSFP-DD SR4 with OM4 provides 400GbE over short distances (up to 100m) using multi-mode fiber. DR4 and FR4 require single- mode fiber and are typically used for longer distances. LR8 typically requires single mode fibre for specified distances. Using SR8 with OM3 is unlikely to achieve 400GbE as SR8 works best with OM4/OM5.

NEW QUESTION # 300
A user reports that their GPU-accelerated application is crashing with a CUDA error related to 'out of memory'. You have confirmed that the GPU has sufficient physical memory What are the likely causes and troubleshooting steps?

- A. The process has exceeded the maximum number of GPU contexts allowed. Reduce the number of concurrent CUDA applications running on the GPU.
- B. The system's virtual memory is exhausted. Increase the swap space.
- C. The application is leaking GPU memory. Use a memory profiling tool like 'cuda-memcheck' to identify the source of the leak.
- D. The application is requesting a larger block of memory than is available in a single allocation. Try breaking the allocation into smaller chunks or using managed memory.
- E. The CUDA driver version is incompatible with the CUDA runtime version used by the application. Update the CUDA driver to match the runtime version.

Answer: C,D

Explanation:
Memory leaks and single-allocation limits are common causes of 'out of memory' errors, even when sufficient physical memory exists. 'cuda-memcheck' is specifically designed to find memory errors in CUDA applications. While driver incompatibility is possible, leaks and allocation size limits are more frequent occurrences.

NEW QUESTION # 301
After configuring MIG on an NVIDIAAIOO GPU, you run 'nvidia-smu and observe that all MIG instances are in the 'Disabled' state.
Which of the following are potential reasons for this issue? (Select all that apply)

- A. The MIG instances are correctly configured but have not been allocated to any processes.
- B. The system's power supply is insufficient.
- C. The GPU is not in MIG mode.
- D. The 'nvidia-persistenced' service is not running.

- E. The necessary NVIDIA drivers are not installed or are incompatible with the GPU.

Answer: C,D,E

Explanation:
MIG instances being in a 'Disabled' state indicates a fundamental problem with the MIG configuration. Incompatible drivers (A) will prevent the instances from being properly initialized. If the GPU is not explicitly placed into MIG mode (B), no MIG instances will be available. The 'nvidia-persistenced' service (D) ensures that driver state persists across reboots, and its absence can cause MIG instances to revert to a disabled state. While unallocated instances (C) will exist, they should be in an 'Idle' state, not 'Disabled'. An insufficient power supply (E) might prevent the GPU from functioning correctly, but it's less likely to specifically cause a 'Disabled' MIG state.

NEW QUESTION # 302
You have created MIG instances on an A100 GPU and want to dynamically adjust their size based on workload demands. Which of the following methods is the most appropriate for automatically resizing MIG instances in response to changing resource requirements?

- A. Adjust the application code to use less GPIJ memory dynamically.
- B. Utilize CUDA MPS to dynamically allocate GPU resources to different processes.
- C. Leverage a GPU virtualization platform with dynamic resource allocation capabilities that integrates with MIG.
- D. Implement a script that monitors GPU utilization and automatically adjusts Kubernetes resource quotas to match.
- E. Use 'nvidia-smi' to manually destroy and recreate MIG instances with different sizes as needed.

Answer: C

Explanation:
Explanation: Dynamically resizing MIG instances requires a mechanism that can automatically adjust the underlying GPU partitioning based on workload demands. The most appropriate method is leveraging a GPU virtualization platform (C) that offers dynamic resource allocation and integrates with MIG. These platforms can monitor resource utilization and automatically resize MIG instances accordingly. Manually resizing (A) is impractical for dynamic adjustments. Kubernetes resource quotas (B) control container resource limits, not the underlying MIG configuration. CUDA MPS (D) allows sharing a single GPU but doesn't resize MIG instances. Adjusting application code (E) doesn't address the need for dynamic MIG resizing.

NEW QUESTION # 303
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