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NVIDIA NCP-AIO Exam

NVIDIA Certified Professional AI Operations

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NVIDIA NCP-AIO Exam Syllabus Topics:

Topic	Details
Topic 1	<ul style="list-style-type: none"> • Troubleshooting and Optimization: NVI This section of the exam measures the skills of AI infrastructure engineers and focuses on diagnosing and resolving technical issues that arise in advanced AI systems. Topics include troubleshooting Docker, the Fabric Manager service for NVIDIA NVlink and NVSwitch systems, Base Command Manager, and Magnum IO components. Candidates must also demonstrate the ability to identify and solve storage performance issues, ensuring optimized performance across AI workloads.
Topic 2	<ul style="list-style-type: none"> • Workload Management: This section of the exam measures the skills of AI infrastructure engineers and focuses on managing workloads effectively in AI environments. It evaluates the ability to administer Kubernetes clusters, maintain workload efficiency, and apply system management tools to troubleshoot operational issues. Emphasis is placed on ensuring that workloads run smoothly across different environments in alignment with NVIDIA technologies.
Topic 3	<ul style="list-style-type: none"> • Installation and Deployment: This section of the exam measures the skills of system administrators and addresses core practices for installing and deploying infrastructure. Candidates are tested on installing and configuring Base Command Manager, initializing Kubernetes on NVIDIA hosts, and deploying containers from NVIDIA NGC as well as cloud VMI containers. The section also covers understanding storage requirements in AI data centers and deploying DOCA services on DPU Arm processors, ensuring robust setup of AI-driven environments.
Topic 4	<ul style="list-style-type: none"> • Administration: This section of the exam measures the skills of system administrators and covers essential tasks in managing AI workloads within data centers. Candidates are expected to understand fleet command, Slurm cluster management, and overall data center architecture specific to AI environments. It also includes knowledge of Base Command Manager (BCM), cluster provisioning, Run.ai administration, and configuration of Multi-Instance GPU (MIG) for both AI and high-performance computing applications.

NVIDIA AI Operations Sample Questions (Q41-Q46):

NEW QUESTION # 41

A system administrator is experiencing issues with Docker containers failing to start due to volume mounting problems. They suspect the issue is related to incorrect file permissions on shared volumes between the host and containers.

How should the administrator troubleshoot this issue?

- **A. Use the docker logs command to review the logs for error messages related to volume mounting and permissions.**
- B. Reinstall Docker to reset all configurations and resolve potential volume mounting issues.
- C. Disable all shared folders between the host and container to prevent volume mounting errors.
- D. Reduce the size of the mounted volumes to avoid permission conflicts during container startup.

Answer: A

Explanation:

Comprehensive and Detailed Explanation From Exact Extract:

The first step to troubleshoot Docker container volume mounting issues is to check the container logs using `docker logs` for detailed error messages, including those related to permissions. This provides direct insight into the cause of the failure. Reinstalling Docker or disabling shared folders are drastic steps and may not address the root cause. Volume size reduction is unrelated to permission conflicts.

NEW QUESTION # 42

When using GPUDirect RDMA for inter-GPU communication, what component MUST be supported by the network interface card

(NIC) to ensure optimal performance?

- A. TCP Offload Engine (TOE)
- B. Ethernet Flow Control
- C. Jumbo Frames
- **D. Remote Direct Memory Access (RDMA)**
- E. Quality of Service (QOS)

Answer: D

Explanation:

GPUDirect RDMA requires RDMA support on the NIC. RDMA enables direct memory access between GPUs without CPU intervention, significantly reducing latency and improving bandwidth. While other features like TOE, QOS, flow control, and Jumbo Frames can contribute to overall network performance, they are not fundamental requirements for GPUDirect RDMA to function.

NEW QUESTION # 43

You are deploying a cloud VMI container on AWS using the NVIDIA GPU Cloud (NGC) AMI. You need to ensure that the container has access to a specific S3 bucket containing the training data. Which of the following is the MOST secure and recommended method to grant this access?

- A. Hardcoding AWS credentials directly within the container's application code.
- B. Using AWS Secrets Manager to store the credentials and retrieving them via the AWS CLI within the container.
- C. Storing AWS credentials in environment variables within the Dockerfile used to build the container image.
- D. Creating a dedicated IAM user and distributing its credentials via AWS Systems Manager Parameter Store. Retrieve credentials during container startup.
- **E. Using an IAM role assigned to the EC2 instance running the VMI container.**

Answer: E

Explanation:

Using an IAM role assigned to the EC2 instance is the most secure method. It avoids storing credentials within the container itself, relying instead on the AWS infrastructure's built-in security mechanisms. Option D is viable but more complex than simply using an IAM Role.

NEW QUESTION # 44

Consider the following code snippet using NVSHMEM:

If this program hangs indefinitely after 'nvshmem_barrier_all()' inside the 'if(my_pe 0)' block, what is the MOST likely cause?

- **A. PE 0 is waiting for other PEs to complete within the loop, but other PEs have already finished and finalized.**
- B. Incorrect use of 'MPI_Barrier'.
- C. Deadlock due to insufficient NVLink bandwidth.
- D. Incorrect use of 'CUDA_VISIBLE_DEVICES'
- E. Memory corruption in 'nvshmem_malloc'.

Answer: A

Explanation:

The issue is a classic synchronization problem. PE 0 is the only PE entering the loop, and it calls to get the values from other PEs. However, the other PEs have already proceeded past the first, executed, and potentially even before PE 0 attempts to read their values. Thus, PE 0 is waiting for PEs that are no longer in a state to respond. MPI_Barrier isn't the issue, nor is NVLink bandwidth. Memory corruption or CUDA_VISIBLE_DEVICES issues would likely cause crashes, not hangs at this specific point.

NEW QUESTION # 45

You are configuring networking for a new AI cluster in your data center. The cluster will handle large-scale distributed training jobs that require fast communication between servers.

What type of networking architecture can maximize performance for these AI workloads?

- A. Use standard Ethernet networking with a focus on increasing bandwidth through multiple connections per server.

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