

FlashArray-Implementation-Specialist Certified Questions | FlashArray-Implementation-Specialist Real Dump



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However, you should keep in mind that to get success in the Pure Storage Certified FlashArray Implementation Specialist (FlashArray-Implementation-Specialist) exam is not an easy task. It is a challenging exam and not a traditional exam. But complete Pure Storage FlashArray-Implementation-Specialist exam preparation can enable you to crack the Pure Storage FlashArray-Implementation-Specialist exam easily. For the quick and complete Pure Storage Certified FlashArray Implementation Specialist (FlashArray-Implementation-Specialist) exam preparation you can trust FlashArray-Implementation-Specialist exam practice test questions. The Pure Storage FlashArray-Implementation-Specialist exam practice test questions have already helped many Pure Storage FlashArray-Implementation-Specialist exam candidates in their preparation and success and you can also trust "UpdateDumps" exam questions and start preparing today.

Pure Storage FlashArray-Implementation-Specialist Exam Syllabus Topics:

Topic	Details
Topic 1	<ul style="list-style-type: none">• Upgrades: This section of the exam measures the skills of FlashArray Implementation Specialists and focuses on tasks involved in managing firmware and software upgrades. Candidates must demonstrate knowledge of upgrade planning, verification steps, and rollback procedures, ensuring that systems are updated with minimal disruption to service.
Topic 2	<ul style="list-style-type: none">• Installation: This section of the exam measures the skills of Enterprise Infrastructure Technicians and focuses on executing a successful installation of FlashArray systems. It tests the ability to perform physical setup, cabling, configuration of network settings, and the application of initial system configurations necessary for full deployment.
Topic 3	<ul style="list-style-type: none">• Post-Installation• Upgrade: This section of the exam measures the skills of FlashArray Implementation Specialists and evaluates how professionals confirm system functionality after installation or an upgrade. It involves validating connectivity, running health checks, confirming configurations, and ensuring that the deployment meets operational expectations.
Topic 4	<ul style="list-style-type: none">• Pre-Installation• Upgrade: This section of the exam measures the skills of Enterprise Infrastructure Technicians and covers all preparation activities before deploying or upgrading a Pure Storage FlashArray. It includes understanding environmental requirements, verifying prerequisites, checking compatibility, and validating system readiness through appropriate tools and documentation.

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Pure Storage Certified FlashArray Implementation Specialist Sample Questions (Q90-Q95):

NEW QUESTION # 90

A customer has filled out the Installation Workbook. The management IP information netmask is specified as "255.255.255.223" and the gateway is specified as "10.24.18.1". What corrective action should be taken to reconcile the IP configuration?

- A. The gateway information should be changed to 10.24.18.223.
- **B. The netmask value should be changed to 255.255.255.0.**
- C. The netmask value should be changed to /23.

Answer: B

Explanation:

To reconcile the IP configuration, the Implementation Engineer must correct the invalid netmask to the standard Class C subnet mask of 255.255.255.0 (Option B).

The netmask provided in the workbook, 255.255.255.223, is mathematically invalid for a subnet mask. Subnet masks function by having a continuous sequence of binary '1's followed by '0's. The decimal value 223 (binary 11011111) breaks this rule due to the zero bit in the middle of the octet. Therefore, the array initialization script would reject this value as a syntax error.

In most standard implementation scenarios where a gateway of 10.24.18.1 is provided, the intended network is a /24 (255.255.255.0), which allows for hosts 10.24.18.1 through 10.24.18.254. While Option C (/23) is a valid subnet mask, it assumes a larger network scope than is typically implied by a standard typo. Correcting the mask to the standard /24 is the safest and most logical remediation to ensure the array can communicate with the gateway and the management network.

NEW QUESTION # 91

During an NDU, when is the Implementation Engineer required to install additional NVRAM modules in slots 2 and 3?

- A. Upgrading a FlashArray//X50R3 to a FlashArray//X50R4
- **B. Upgrading a FlashArray//X50R3 to a FlashArray//X70R4**
- C. Upgrading a FlashArray//X70R4 to a FlashArray//X90R4

Answer: B

Explanation:

FlashArray models differ in their NVRAM requirements based on their performance tier and throughput capabilities. Lower and mid-range models (like the //X10, //X20, and //X50) typically operate with two NVRAM modules installed in slots 0 and 1. However, high-performance models (like the //X70 and //X90) require double the NVRAM buffer to handle the increased write I/O bandwidth, necessitating four NVRAM modules (populating slots 0, 1, 2, and 3).

When performing a Data-in-Place Upgrade (NDU) from a FlashArray//X50R3 to a FlashArray//X70R4, the chassis remains (or is upgraded in a way that preserves data), but the new controller configuration shifts from a "2-NVRAM" requirement to a "4-NVRAM" requirement. Therefore, as part of the physical upgrade procedure, the Implementation Engineer must install two additional NVRAM modules into slots 2 and 3 of the chassis to support the new //X70 controllers.

If these modules are not added, the new //X70 controllers will detect an "insufficient hardware" condition and fail to boot or initialize the Purity software properly. This step is unique to upgrades crossing the boundary between mid-range and high-end models.

NEW QUESTION # 92

An Implementation Engineer performs a controller upgrade. What is the command to check I/O health?

- A. purehost monitor --balance
- B. purehost list --all
- C. purereport list --initiator

Answer: A

Explanation:

The command purehost monitor --balance checks the distribution and health of I/O across hosts and initiators, helping verify balanced I/O after a controller upgrade.

NEW QUESTION # 93

During an NDU, when is the Implementation Engineer required to install additional NVRAM modules in slots 2 and 3?

- A. Upgrading a FlashArray//X50R3 to a FlashArray//X50R4
- B. Upgrading a FlashArray//X50R3 to a FlashArray//X70R4
- C. Upgrading a FlashArray//X70R4 to a FlashArray//X90R4
- D. Upgrading a FlashArray//X10R3 to a FlashArray//X20R4

Answer: B

Explanation:

This scenario highlights a critical architectural transition point within the Pure Storage FlashArray product line. The arrays utilize dedicated NVRAM modules to safely ingest and acknowledge incoming writes before destaging them to the capacity flash drives. On the lower to mid-range models (such as the //X10, //X20, and //X50), the system requires only two NVRAM modules to maintain a highly available, mirrored write cache. These are physically installed in the central chassis slots NVB0 and NVB1. However, the higher-end, performance-heavy models (such as the //X70 and //X90) boast significantly more processing power and ingest bandwidth. To support these massive workloads without creating a cache bottleneck, the architecture mandates four NVRAM modules, filling slots NVB0, NVB1, NVB2, and NVB3. Therefore, when performing an inter-generational, cross-model Hardware Non-Disruptive Upgrade (HWNDU) from an //X50 to an //X70, the Implementation Engineer is traversing that architectural boundary.

While performing the controller swaps, the engineer is strictly required to install two additional, newly shipped NVRAM modules into the previously empty bays NVB2 and NVB3. If they fail to add these modules, the new //X70 controllers will detect a cache hardware deficiency and refuse to initialize, halting the upgrade process. Options B, C, and D do not cross this 2-to-4 NVRAM threshold.

NEW QUESTION # 94

Which NVRAM bays are always populated on the FlashArray//XR2/3?

- A. 0-1
- B. 0-3
- C. 0-2

Answer: A

Explanation:

The FlashArray//XR2 and //XR3 architectures utilize dedicated NVRAM modules to provide the non-volatile write cache necessary for acknowledging writes safely before they are destaged to flash. These chassis are equipped with multiple NVRAM bays (labeled NVB0 through NVB3).

For standard configurations, specifically the //X10, //X20, and //X50 models, the system requires a redundant pair of NVRAM modules to function. These are always installed in NVRAM bays 0 and 1.

Bays 0 and 1 form the primary high-availability pair. If one fails, the other retains the data, and the system can continue to operate (though often in write-through mode or with alerts).

Higher-end models like the //X70 and //X90 populate all four bays (0-3) to provide the larger write buffer required for their higher throughput capabilities.

However, since the question asks which bays are always populated (i.e., the minimum requirement for the platform to function across the board), the answer is the foundational pair in slots 0 and 1. An Implementation Engineer must ensure these specific slots

are populated first during any chassis maintenance or upgrade.

NEW QUESTION # 95

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