売れ筋ランキングナンバーワンFlashArray-Implementation-Specialistを効率よくマスター

2026年Fast2testの最新FlashArray-Implementation-Specialist PDFダンプおよびFlashArray-Implementation-Specialist試験エンジンの無料共有：https://drive.google.com/open?id=1Ur7bIzOZZRliHKsNRobWtS1277oaDmH5

Fast2testはPure StorageのFlashArray-Implementation-Specialist「Pure Storage Certified FlashArray Implementation Specialist」試験に関する完全な資料を唯一のサービスを提供するサイトでございます。Fast2testが提供した問題集を利用してPure StorageのFlashArray-Implementation-Specialist試験は全然問題にならなくて、高い点数で合格できます。Pure Storage FlashArray-Implementation-Specialist試験の合格のために、Fast2testを選択してください。

Pure Storage FlashArray-Implementation-Specialist 認定試験の出題範囲：

トピック	出題範囲
トピック 1	• インストール後 / アップグレード：このセクションでは、FlashArray実装スペシャリストのスキルを測定し、インストール後またはアップグレード後のシステム機能の確認方法を評価します。接続性の検証、ヘルスチェックの実行、構成の確認、そして導入が運用上の期待を満たしていることの確認などが含まれます。
トピック 2	• インストール前 • アップグレード：このセクションでは、エンタープライズ・インフラストラクチャ技術者のスキルを評価し、Pure Storage FlashArrayの導入またはアップグレード前のすべての準備作業を網羅します。環境要件の理解、前提条件の検証、互換性の確認、適切なツールとドキュメントによるシステムの準備状況の検証などが含まれます。
トピック 3	• インストール：このセクションでは、エンタープライズ・インフラストラクチャ技術者のスキルを評価し、FlashArrayシステムの適切なインストールの実行に焦点を当てます。物理的なセットアップ、ケーブル配線、ネットワーク設定の構成、そして完全な導入に必要な初期システム構成の適用能力がテストされます。

トピック４	• アップグレード：このセクションでは、FlashArray実装スペシャリストのスキルを評価し、ファームウェアおよびソフトウェアのアップグレード管理に関連するタスクに焦点を当てます。受験者は、アップグレードの計画、検証手順、およびロールバック手順に関する知識を証明し、サービスへの影響を最小限に抑えながらシステムを更新する必要があります。

>> FlashArray-Implementation-Specialist日本語版対応参考書 <<

Pure Storage FlashArray-Implementation-Specialist日本語参考 & FlashArray-Implementation-Specialist復習攻略問題

幸せの生活は自分で作られて得ることです。だから、大人気なIT仕事に従事したいあなたは今から準備して努力するのではないでしょうか？さあ、ここで我々社のPure StorageのFlashArray-Implementation-Specialist試験模擬問題を推薦させてくださいませんか。我が社のFlashArray-Implementation-Specialist問題集は必ずあなたの成功へ道の助力になれます。

Pure Storage Certified FlashArray Implementation Specialist 認定 FlashArray-Implementation-Specialist 試験問題 (Q112-Q117):

質問 # 112
An Implementation Engineer is performing a capacity consolidation on an X50R2 that is 83% full. The starting config is X50R2-20/10-22/0. The end config is X50R2-91/0. What steps should the Implementation Engineer follow to complete the work?

- A. Evac the chasses drives, install the new 91TB chassis data pack, and evac SH0.
- B. Evac SH0, evac the 20TB chassis DP, install the new 91TB DP, and evac the 10TB chassis DP.
- C. Move the 20TB chassis DP to SH0, install the new 91TB chassis DP, evac the 10TB chassis DP, and evac SH0.

正解：C

解説：
The correct procedure is to move the 20TB chassis Data Pack (DP) to the external shelf (SH0) to free up a chassis slot for the new high-capacity pack.
* Capacity Analysis: The array has ~52TB total raw capacity (20+10+22) and is 83% full (~43TB used).
* Constraint: You cannot simply "evac" the chassis drives (Option A) because the remaining capacity (22TB in the shelf) is insufficient to hold the 43TB of data. Similarly, evacuating the shelf first (Option C) leaves only 30TB in the chassis, which is also insufficient.
* The Solution (Option B):
* Relocate: The engineer physically moves the 20TB pack from the Chassis (Slot 0) to the empty slot in Shelf 0 (SH0). This is a supported non-disruptive operation (assuming compatible shelf
/pack types).
* Install: This clears Chassis Slot 0, allowing the installation of the massive 91TB Data Pack .
* Consolidate: The system now has huge capacity (10+20+22+91). Purity can safely evacuate the old 10TB pack (Chassis) and the external shelf packs (20+22) into the new 91TB internal capacity.
* Final State: The array ends up with the single 91TB pack in the chassis, meeting the "X50R2-91
/0" target configuration.

質問 # 113
An Implementation Engineer is installing the first DirectFlash Shelf (DFS) on a FlashArray//XR4 controller. Which ports should the Implementation Engineer use for the connection?

- A. ETH 0/1
- B. ETH 18/19
- C. ETH 10/11

正解：B

解説：
The FlashArray//XR4 utilizes onboard 100GbE RoCE (RDMA over Converged Ethernet) ports for connecting to the first loop of DirectFlash Shelves, removing the need for dedicated PCIe add-in cards for the base configuration. The port mapping for these onboard backend ports differs from previous generations.
On the //XR4 controller chassis, the specific Ethernet interfaces designated for the first DirectFlash Shelf loop are typically the high-numbered onboard ports. In this specific exam context and hardware revision, ETH 18 and ETH 19 are identified as the correct primary backend connectivity ports .
Implementation Engineers must connect the QSFP28 cables from the "uplink" ports on the first shelf (IOM0 and IOM1) to ETH18 and ETH19 on the controllers (CT0 and CT1) respectively. Using incorrect ports (like ETH0/1, which are management) would result in the shelf not being detected by the storage fabric.
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質問 # 114
Before leaving the site after an install, who is required to provide completion sign-off?

- A. Account team
- B. Customer
- C. Support

正解： B

解説：
The Customer is the mandatory party required to provide completion sign-off before the Implementation Engineer leaves the site. According to the FlashArray Implementation Service Brief and standard operating procedures, the final step of the deployment phase is the "Project Sign-off." After the engineer has racked, cabled, initialized, and verified the array's health (often demonstrating connectivity and the Purity GUI), the customer must formally acknowledge that the installation meets the agreed-upon scope of work. This sign-off serves as the legal and operational acceptance of the hardware and software implementation.
While the Account Team manages the commercial relationship and Support may assist with technical hurdles, neither can validate that the physical implementation meets the customer's specific on-site requirements or that the "handover" of administration has occurred. The customer's signature (physical or digital) on the implementation checklist or service completion form signals the transition from the "Deployment" phase to the "Production/Support" phase.

質問 # 115
An Implementation Engineer is performing a FlashArray//XR4 to FlashArray//XL upgrade, and is preparing to remove the XR4 CT0 controller.
Which command should the Implementation Engineer run to stop Purity after confirming CT0 is the secondary controller?

- A. purewes controller disable --purity --verify-mode secondary --verify-array ARRAY_NAME ct0
- B. purehw controller stop --verify-array ARRAY_NAME ct0
- C. purewes controller disable --purity --verify-mode primary --verify-array ARRAY_NAME ct0
- D. purewes controller setattr --disable --verify-mode secondary --verify-array ARRAY_NAME ct0

正解： A

解説：
During a complex cross-generational Hardware Non-Disruptive Upgrade (HWNDU) from a FlashArray//XR4 to a FlashArray//XL, the Implementation Engineer must carefully orchestrate the shutdown and removal of the legacy compute nodes. Before physically unlatching and removing the CT0 controller, the Purity//FA operating system processes running on that specific node must be gracefully halted.
Because standard user commands do not allow for this level of low-level service manipulation, the engineer must utilize the purewes (Wessex) command-line suite. Wessex is the internal engineering toolkit used to override high-availability states during hardware replacements.
The correct command to execute is purewes controller disable --purity --verify-mode secondary --verify- array < ARRAY_NAME > ct0 .
This command contains several critical safety mechanisms. The --purity flag specifies that the script should shut down the Purity storage processes rather than just network interfaces. The --verify-mode secondary flag is a mandatory safeguard; it checks the cluster state to guarantee that CT0 has already successfully failed over its active host I/O responsibilities to CT1. If CT0 is still the primary controller, the command will intentionally fail to prevent an accidental storage outage. Finally, --verify-array ensures the engineer is executing this disruptive command on the correct physical array, which is crucial when multiple SSH sessions are open in

a busy datacenter.

質問 # 116
What is the default FlashArray//XLR5 - FC configuration shipped for new installs?

- A. 4-port 32G FC card with 32G optics installed in PCIe slot 3; 2-Port 32G FC card with 32G optics installed in PCIe slot 5; DirectCompress Accelerator installed in PCIe slot 6
- B. 4-port 32G FC card with 32G optics installed in PCIe Slot 1; 2-Port 32G FC card with 32G optics installed in PCIe slot 3; DirectCompress Accelerator installed in PCIe slot 8
- C. 4-Port 32G FC card with 32G optics installed in PCIe slot 4; 2-Port 32G FC card with 32G optics installed in PCIe slot 3; DirectCompress Accelerator installed in PCIe slot 5

正解：A

解説：
The FlashArray//XL series has a specific, validated slot population rule for its PCIe cards to ensure optimal performance and thermal management. For a standard Fibre Channel (FC) configuration on a FlashArray
//XLR5 , the factory default shipment includes a specific arrangement of host I/O cards and offload engines.
The correct default configuration is:
* PCIe Slot 3: 4-Port 32Gb FC card.
* PCIe Slot 5: 2-Port 32Gb FC card.
* PCIe Slot 6: DirectCompress Accelerator (DCA).
This layout (Option B) adheres to the //XL's architecture where specific slots are prioritized for frontend host I
/O (like slots 3 and 5) and others for offload functions or backend connectivity. Implementation Engineers must verify cards are in these exact slots during the visual inspection. If cards are moved to incorrect slots (like slot 1 or 4 for primary FC in this specific config), the array may not boot or may fail to initialize the FC services correctly due to BIOS enumeration orders.
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質問 # 117
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どのようにPure Storage　FlashArray-Implementation-Specialist試験に準備すると悩んでいますか。我々社のFlashArray-Implementation-Specialist問題集を参考した後、ほっとしました。弊社のFlashArray-Implementation-Specialistソフト版問題集はかねてより多くのIT事業をしている人々は順調にPure Storage　FlashArray-Implementation-Specialist資格認定を取得させます。試験にパースする原因は我々問題集の全面的で最新版です。

FlashArray-Implementation-Specialist日本語参考: https://jp.fast2test.com/FlashArray-Implementation-Specialist-premium-file.html

- FlashArray-Implementation-Specialist合格内容 □ FlashArray-Implementation-Specialist過去問 □ FlashArray-Implementation-Specialist試験準備 ✔ 《 www.xhs1991.com 》を開き、□ FlashArray-Implementation-Specialist □を入力して、無料でダウンロードしてくださいFlashArray-Implementation-Specialist試験問題集
- 素晴らしいFlashArray-Implementation-Specialist日本語版対応参考書一回合格-真実的なFlashArray-Implementation-Specialist日本語参考 □ □ www.goshiken.com □を開き、▹ FlashArray-Implementation-Specialist ◃を入力して、無料でダウンロードしてくださいFlashArray-Implementation-Specialist試験問題集
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