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To be successful in a professional exam like the Pure Storage FlashArray-Implementation-Specialist exam, you must know the criteria to pass it. You should know the type of Pure Storage Certified FlashArray Implementation Specialist questions, the pattern of the Pure Storage Certified FlashArray Implementation Specialist exam, and the time limit to complete the FlashArray-Implementation-Specialist Exam. All these factors help you pass the Pure Storage FlashArray-Implementation-Specialist exam. Actual4Dumps is your reliable partner in getting your FlashArray-Implementation-Specialist certification. The Pure Storage FlashArray-Implementation-Specialist exam dumps help you achieve your professional goals.

## Pure Storage FlashArray-Implementation-Specialist Exam Syllabus Topics:

Topic	Details
Topic 1	<ul style="list-style-type: none"> <li>• Post-Installation</li> <li>• Upgrade: This section of the exam measures the skills of FlashArray Implementation Specialists and evaluates how professionals confirm system functionality after installation or an upgrade. It involves validating connectivity, running health checks, confirming configurations, and ensuring that the deployment meets operational expectations.</li> </ul>
Topic 2	<ul style="list-style-type: none"> <li>• Upgrades: This section of the exam measures the skills of FlashArray Implementation Specialists and focuses on tasks involved in managing firmware and software upgrades. Candidates must demonstrate knowledge of upgrade planning, verification steps, and rollback procedures, ensuring that systems are updated with minimal disruption to service.</li> </ul>
Topic 3	<ul style="list-style-type: none"> <li>• Pre-Installation</li> <li>• Upgrade: This section of the exam measures the skills of Enterprise Infrastructure Technicians and covers all preparation activities before deploying or upgrading a Pure Storage FlashArray. It includes understanding environmental requirements, verifying prerequisites, checking compatibility, and validating system readiness through appropriate tools and documentation.</li> </ul>

Topic 4	<ul style="list-style-type: none"><li>• Installation: This section of the exam measures the skills of Enterprise Infrastructure Technicians and focuses on executing a successful installation of FlashArray systems. It tests the ability to perform physical setup, cabling, configuration of network settings, and the application of initial system configurations necessary for full deployment.</li></ul>
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### **Pure Storage Certified FlashArray Implementation Specialist Sample Questions (Q103-Q108):**

#### **NEW QUESTION # 103**

What is the required PSU wattage for a FlashArray// X70R2/R3 or X90R2/R3 array?

- A. 1200W
- B. 1300W
- **C. 1600W**

**Answer: C**

Explanation:

The FlashArray//X70R2 and //X90R2 models are equipped with dual 1600W power supplies, each rated for 1600 watts. These high-wattage power supplies are designed to support the increased performance and capacity demands of these models. The power supplies operate in an N+1 redundancy configuration, ensuring continuous operation even if one power supply fails.

#### **NEW QUESTION # 104**

For FlashArray//XL, which PCIe card slot is reserved for control upgrades?

- A. 0
- **B. 1**
- C. 2

**Answer: B**

Explanation:

On the FlashArray//XL, PCIe Slot 5 is explicitly reserved for Non-Disruptive Upgrade (NDU) procedures and specific control plane functions.

The FlashArray//XL chassis (5U) features a redesigned controller layout with 9 PCIe slots per controller (numbered 0-8). While slots 0-3 and 6-8 are generally available for Host I/O (FC/iSCSI) or Backend Expansion (SAS/NVMe-oF), Slot 5 is kept open or populated with specific interconnect cards used during controller upgrades.

During a "Data-in-Place" upgrade (e.g., moving from an //X to an //XL or upgrading //XL generations), this slot facilitates the high-speed link required to cluster the old and new controllers or to transfer state information. Implementation Engineers must ensure this slot is not populated with standard customer I/O cards (like FC HBAs) during the initial configuration, as doing so would block the ability to perform future non-disruptive hardware upgrades.

### NEW QUESTION # 105

After upgrading Purity during a new installation, which command should an Implementation Engineer run to determine what version of Purity will be loaded at the next boot of the FlashArray?

- A. pureversion -a
- **B. pureboot list**
- C. purearray list --controller

**Answer: B**

Explanation:

The command pureboot list is the correct tool to verify which Purity version is designated to load upon the next system reboot. FlashArray controllers maintain two boot partitions (often referred to as banks) for redundancy and upgrade safety. When an upgrade is performed, the new Purity software image is written to the inactive bank.

\* Verification: Running pureboot list displays the status of both boot banks (e.g., Primary and Secondary) and explicitly flags which version is "Running" and which is set for "Next Boot."

\* Why it's critical: Just checking the currently running version (using pureversion or purearray list) is insufficient during an upgrade workflow. An engineer must confirm that the boot pointer has successfully switched to the new version before issuing the reboot command. If pureboot list shows the old version as "Next Boot," a reboot would simply reload the current code, indicating the upgrade installation failed or wasn't finalized.

### NEW QUESTION # 106

When installing the first DirectFlash Shelf on a FlashArray//XR2/3, what mezzanine ports are used on the controllers?

- A. ETH7 and ETH9
- **B. ETH6 and ETH9**
- C. ETH6 and ETH8

**Answer: B**

Explanation:

When installing the first DirectFlash Shelf (DFS) on a FlashArray//XR2 or //XR3 utilizing the onboard or default mezzanine connectivity, the correct ports to use are ETH6 and ETH9.

Hardware Layout: The FlashArray//XR2 and //XR3 utilize a specific Ethernet Mezzanine (EMEZ) slot for backend connectivity to NVMe-oF shelves. This card typically enumerates its ports as ETH6 through ETH9 (a 4-port configuration).

Cabling Rule: To maximize physical separation and ensure redundancy across the internal ASIC lanes of the mezzanine card, standard cabling procedures for the first shelf dictate using the outer ports: ETH6 (Port 0) and ETH9 (Port 3).

Redundancy: This configuration (Port 0 and Port 3) is preferred over using adjacent ports (like 6 and 7) to reduce the risk of a single lane cluster failure affecting both connections to the shelf. The second shelf, if added later, would typically utilize the remaining ports (ETH7 and ETH8).

### NEW QUESTION # 107

Prior to installing a new FlashArray controller, which terminal display settings must the Implementation Engineer configure?

- A. 8-E-1, Xon/Xoff flow control, 115,200 baud
- **B. 8-N-1, No flow control, 115,200 baud**
- C. 8-N-1, Xon/Xoff flow control, 38,400 baud
- D. 7-E-1, Hardware flow control, 9,600 baud

**Answer: B**

Explanation:

When unpacking and installing a new Pure Storage FlashArray controller, the initial configuration—such as assigning management IP addresses, subnet masks, and gateways via the puresetup utility—must be done completely out-of-band. Because the array does not yet have an IP presence on the customer's network, the Implementation Engineer must connect directly to the controller's serial console port using a standard serial-to-USB adapter and terminal emulation software (such as PuTTY, SecureCRT, or Tera Term). To successfully establish a readable CLI session with the custom Linux microkernel running on the FlashArray controllers, the terminal emulator must be configured with the exact serial communication parameters expected by the array. According to Pure Storage hardware documentation, the mandatory COM port settings are:

- \* Baud Rate (Speed): 115,200
- \* Data Bits: 8
- \* Parity: None (N)
- \* Stop Bits: 1
- \* Flow Control: None

This configuration is universally abbreviated as 115200 8-N-1 . If an engineer attempts to use older standard speeds (like 9600 or 38400 baud) or leaves flow control (like Xon/Xoff or RTS/CTS) enabled, the terminal window will display unreadable, garbled characters (often referred to as "wingdings"), or the keyboard inputs will fail to register entirely, preventing the initialization of the array.

## NEW QUESTION # 108

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