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Huawei H31-341_V2.5: HCIP-Transmission V2.5 exam is an important certification for professionals in the telecommunications industry. It is designed to test candidates' knowledge and skills in transmission network technologies and is recognized globally. H31-341_V2.5-ENU Exam covers a wide range of topics and is aimed at individuals who have experience in transmission network technologies and wish to advance their careers in this field. Those who pass the exam will have the opportunity to demonstrate their expertise in transmission network technologies and will be highly valued in the telecommunications industry.

Huawei HCIP-Transmission V2.5 Sample Questions (Q24-Q29):

NEW QUESTION # 24

The UITL board belongs to the optical multiplexing and demultiplexing boards. The UITL board realizes the multiplexing and demultiplexing of () GHz interval and () GHz interval signals.

Answer:

Explanation:

50 | 100

NEW QUESTION # 25

The OptiXtrans DC908 chassis can be a DC chassis or an AC chassis.

- A. TRUE
- B. FALSE

Answer: A

Explanation:

The OptiXtrans DC908 chassis supports both DC and AC power supply configurations, allowing it to operate as either a DC chassis or an AC chassis.

NEW QUESTION # 26

Which of the following statements about overhead bytes is wrong?

- A. E1 and E2 are wired bytes. These bytes are used to provide voice channels for order line connections.
- B. A1 and A2 are framing bytes. These bytes are used to separate STM-1 frames in the signal stream
- C. B1 is Bit Interleaved Parity-8 (BIP-8). This byte is used to monitor the regenerator part layer for bit errors
- D. JO is the regenerator section trace byte. This byte is used to repeatedly send the segment access point identifier so that the receiving end can verify its continuous connection with the designated sending end

Answer: B

NEW QUESTION # 27

MPLS technology introduces the concept of connection-oriented label switching on the connectionless IP network, and integrates IP technology and ATM label switching technology.

- A. False
- B. True

Answer: B

NEW QUESTION # 28

OTUk_DEG indicates OTUk signal degradation. Which of the following statements about the bit error detection mode are true? (Choose all that apply.)

- A. When bit errors are detected in Poisson mode, this alarm is reported after signal degradation or bit error threshold crossing occurs.
- B. When bit errors are detected in burst mode, this alarm is reported after signal degradation or bit error threshold crossing occurs.
- C. When bit errors are detected in burst mode, this alarm is reported after signal degradation occurs.
- D. When bit errors are detected in Poisson mode, this alarm is reported after signal degradation occurs.

Answer: B,C

Explanation:

In burst mode, an OTUk_DEG alarm is triggered when bit errors cause signal degradation, even if the error count threshold isn't crossed.

In burst mode, the alarm also occurs if the number of bit errors within the detection window exceeds the configured threshold.

NEW QUESTION # 29

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