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Pure Storage FlashArray-Implementation-Specialist Exam Syllabus Topics:

Topic	Details

Topic 1	<ul style="list-style-type: none"> • Pre-Installation • Upgrade: This section of the exam measures the skills of Enterprise Infrastructure Technicians and covers all preparation activities before deploying or upgrading a Pure Storage FlashArray. It includes understanding environmental requirements, verifying prerequisites, checking compatibility, and validating system readiness through appropriate tools and documentation.
Topic 2	<ul style="list-style-type: none"> • Installation: This section of the exam measures the skills of Enterprise Infrastructure Technicians and focuses on executing a successful installation of FlashArray systems. It tests the ability to perform physical setup, cabling, configuration of network settings, and the application of initial system configurations necessary for full deployment.
Topic 3	<ul style="list-style-type: none"> • Upgrades: This section of the exam measures the skills of FlashArray Implementation Specialists and focuses on tasks involved in managing firmware and software upgrades. Candidates must demonstrate knowledge of upgrade planning, verification steps, and rollback procedures, ensuring that systems are updated with minimal disruption to service.
Topic 4	<ul style="list-style-type: none"> • Post-Installation • Upgrade: This section of the exam measures the skills of FlashArray Implementation Specialists and evaluates how professionals confirm system functionality after installation or an upgrade. It involves validating connectivity, running health checks, confirming configurations, and ensuring that the deployment meets operational expectations.

Pure Storage Certified FlashArray Implementation Specialist Sample Questions (Q89-Q94):

NEW QUESTION # 89

Prior to running the `puresetup` command, which command should an Installation Engineer run on a new install of a FlashArray//XR4 array?

- A. `pureboot list`
- B. `cobalt check . py`
- C. `purehw list`

Answer: B

Explanation:

The FlashArray//XR4 introduces a new hardware platform architecture (internally codenamed or associated with "Cobalt" components for its BMC and chassis management). During the manufacturing and initial deployment phase, specific validation scripts are used to ensure the platform hardware is correctly initialized before the Purity Operating Environment takes over.

For a new install of a FlashArray//XR4, the Implementation Engineer is often required to run `cobalt check . py` (or `cobalt_check.py`). This engineering-level Python script queries the Baseboard Management Controller (BMC) and internal sensors to verify that the chassis components, fans, and power delivery systems are reporting "Healthy" status specifically for the R4 architecture.

Running this before `puresetup` prevents the setup process from failing midway due to a hardware initialization error. It acts as a "pre-flight" hardware validation specific to the R4 generation's new chipset and management subsystem.

NEW QUESTION # 90

An Implementation Engineer is setting up a customer environment and has specified a number of NTP servers. Which command should the Implementation Engineer run to list the NTP servers that have been configured?

- A. `purearray list --ntpserver`
- B. `purenetwork list --ntpserver`
- C. `puredns list --ntpserver`

Answer: B

Explanation:

The command `purenetwork list --ntpserver` displays the configured NTP servers on the FlashArray network settings.

NEW QUESTION # 91

An Implementation Engineer performs a controller upgrade. What is the command to check I/O health?

- A. `pureport list --initiator`
- B. `purehost monitor --balance`
- C. `purehost list --all`

Answer: B

Explanation:

The command to check I/O health and validate connectivity after a controller upgrade is `purehost monitor --balance`.

Following an upgrade (especially after a controller reboot or failover/giveback), it is essential to verify that the host Multipath I/O (MPIO) drivers have re-established connectivity to all paths.

`purehost monitor`: Displays real-time performance statistics (IOPS, Bandwidth, Latency) for connected hosts.

`--balance`: This specific flag breaks down the metrics per initiator (HBA port) for each host.

Why it's used: An Implementation Engineer uses this to look for "imbalanced" flow. If one initiator shows high IOPS and another shows zero, it indicates a dead path, a zoning issue, or an MPIO driver that failed to recover the path to the upgraded controller. Options A and C provide static configuration lists (mappings and WWNs) but do not show live I/O flow, making them insufficient for health monitoring.

NEW QUESTION # 92

Which PCI cards must be validated on the Bill of Materials (BOM) to allow an Implementation Engineer to attach a DirectFlash Shelf to a FlashArray//XL130R5?

- A. FA-XL-100G Ethernet/RoCE 2-Port
- B. FA-XL-200G Ethernet/RoCE 2-Port
- C. FA-XL-100G iSCSI/RoCE 2-Port

Answer: A

Explanation:

The FlashArray//XL series (such as the XL130R5) utilizes a modern backend connectivity architecture for its expansion shelves, specifically the DirectFlash Shelf (DFS). Unlike older generations that might have used SAS (Serial Attached SCSI) for shelf connectivity, the //XL architecture relies on high-speed Ethernet with RoCE (RDMA over Converged Ethernet) to communicate with external media shelves. This ensures that the NVMe drives in the shelf perform with the same low latency as those in the head unit.

To attach a DirectFlash Shelf to a FlashArray//XL130R5, the chassis must be populated with specific PCIe interface cards designated for shelf back-end connectivity. The correct part for this is the FA-XL-100G Ethernet/RoCE 2-Port card. This card provides the necessary 100 Gigabit Ethernet bandwidth and RoCE protocol support required for the NVMe-oF (NVMe over Fabrics) backend fabric that Pure Storage uses.

It is crucial to distinguish this from host-facing cards. While an "iSCSI/RoCE" card exists, the shelf connectivity specifically utilizes the "Ethernet/RoCE" designation in the BOM to differentiate backend fabric ports from frontend host ports. The 200G options are generally reserved for host connectivity or inter-array clustering in specific high-performance setups, but the standard validated card for connecting the DFS to the //XL130R5 is the 100G Ethernet/RoCE adapter. Verifying this line item on the BOM prevents onsite delays where the engineer might otherwise find themselves with incompatible ports for the expansion shelves.

NEW QUESTION # 93

What is the speed of the Ethernet ports to connect a FlashArray//E to a DFS?

- A. 40G
- B. 25G
- C. 100G

Answer: B

Explanation:

The FlashArray//E connects to its DirectFlash Shelves (DFS) using 25Gb Ethernet (RoCE).

The FlashArray//E is the "Capacity Optimized" member of the FlashArray family, designed to replace spinning disk repositories with all-flash efficiency. While the high-performance FlashArray//XL utilizes 100GbE for its backend NVMe-oF connectivity to maximize throughput for mission-critical databases, the //E prioritizes cost-efficiency, density, and power consumption.

Architecture: To maintain this efficiency balance, the backend connectivity for expansion shelves on the //E platform is standardized on 25GbE. This provides ample bandwidth for the QLC-based workloads (typically unstructured data, repository, or Tier 2 block) without the higher cost and power draw of 100GbE optics and controllers.

Comparison: Option A (40G) is a legacy QSFP standard not used in modern Pure backend designs. Option C (100G) is reserved for the performance-tier //XL and high-end //X models.

NEW QUESTION # 94

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