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Exam : FlashArray Implementation Specialist

Title : Pure Storage Certified FlashArray Implementation Specialist (FAIS)

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Pure Storage FlashArray-Implementation-Specialist Exam Syllabus Topics:

Topic	Details
Topic 1	Upgrades: This section of the exam measures the skills of FlashArray Implementation Specialists and focuses on tasks involved in managing firmware and software upgrades. Candidates must demonstrate knowledge of upgrade planning, verification steps, and rollback procedures, ensuring that systems are updated with minimal disruption to service.

Topic 2	• Post-Installation • Upgrade: This section of the exam measures the skills of FlashArray Implementation Specialists and evaluates how professionals confirm system functionality after installation or an upgrade. It involves validating connectivity, running health checks, confirming configurations, and ensuring that the deployment meets operational expectations.
Topic 3	• Installation: This section of the exam measures the skills of Enterprise Infrastructure Technicians and focuses on executing a successful installation of FlashArray systems. It tests the ability to perform physical setup, cabling, configuration of network settings, and the application of initial system configurations necessary for full deployment.
Topic 4	• Pre-Installation • Upgrade: This section of the exam measures the skills of Enterprise Infrastructure Technicians and covers all preparation activities before deploying or upgrading a Pure Storage FlashArray. It includes understanding environmental requirements, verifying prerequisites, checking compatibility, and validating system readiness through appropriate tools and documentation.

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Pure Storage Certified FlashArray Implementation Specialist Sample Questions (Q181-Q186):

NEW QUESTION # 181

For a new FlashArray installation, a customer's network team has NOT provisioned network ports for management connectivity. How will this affect the installation process?

- **A. The customer will be unable to login to the array.**
- B. Management connectivity is required for installation and the setup will fail.
- C. The array's data will be inaccessible.
- D. The array will default to using the Fibre Channel ports for management.

Answer: A

Explanation:

A frequent challenge Implementation Engineers face in the field is arriving onsite for a physical deployment only to discover that the customer's networking team has not yet provisioned the Top-of-Rack (ToR) switches, routed the management VLANs, or provided active copper connections for the array's eth0 and eth1 ports.

Despite this lack of upstream network readiness, the physical installation and logical initialization of the FlashArray can still successfully proceed. The Implementation Engineer can power on the chassis, connect a laptop directly to the primary controller's serial console port, and run the puresetup newarray script. By utilizing the --skip-network-tests flag, the engineer forces the array to accept the configured IP addresses and initialize the storage cluster without requiring an active external network connection. However, the operational consequence of this scenario is that the customer will be unable to log in to the array. While the array is fully built and technically running in the rack, the lack of provisioned management network ports means there is no physical path for the customer to access the web-based Purity GUI, establish an SSH session, or connect via the REST API. Consequently, they cannot provision volumes, configure host groups, or monitor the system until the network team finally activates the switch ports and patches the cables.

NEW QUESTION # 182

On a FlashArray//XR50R2/3 Fibre Channel (FC) array, what is the default type and placement of the PCIe FC card?

- A. 2-port in slot 2
- B. 4-port in slot 0
- C. 2-port in slot 0

Answer: A

Explanation:

On an X50R2/3 FC array, Pure ships each controller with a factory-installed dual-port 32 Gb Fibre Channel card in the PCIe slot numbered 2, providing out-of-the-box SAN connectivity.

NEW QUESTION # 183

After running `puredb run giveback --safe` command once CT0 has been upgraded, how long should the Implementation Engineer wait before continuing with the controller upgrade?

- A. 5 minutes
- B. 10 minutes
- C. 3 minutes

Answer: B

Explanation:

Waiting 10 minutes after running `puredb run giveback --safe` ensures the system completes safe resource rebalancing and stabilizes before proceeding with the next controller upgrade step.

NEW QUESTION # 184

What is the required PSU wattage for a FlashArray// X70R2/R3 or X90R2/R3 array?

- A. 1300W
- B. 1600W
- C. 1200W

Answer: B

Explanation:

The FlashArray//X70R2 and //X90R2 models are equipped with dual 1600W power supplies, each rated for 1600 watts. These high-wattage power supplies are designed to support the increased performance and capacity demands of these models. The power supplies operate in an N+1 redundancy configuration, ensuring continuous operation even if one power supply fails.

NEW QUESTION # 185

Which default speed FC SFPs are shipped in FlashArray//XCR4 FC arrays?

- A. 0
- B. 1
- C. 2

Answer: A

Explanation:

FlashArray//X R4 (and the associated //C R4) models, introduced with the PCIe Gen 4 architecture, feature support for 64Gb Fibre Channel (FC) connectivity. The default Fibre Channel Host Bus Adapters (HBAs) and the accompanying SFPs shipped with this generation are 64Gb capable, marking a significant performance leap from the 32Gb standard of the R3 generation. While the HBAs are backward compatible with 32Gb and 16Gb SAN infrastructures, the R4 platform is explicitly marketed and technically defined by its ability to drive 64Gb line speeds to match the increased throughput of the backend PCIe Gen 4 bus and DirectFlash Modules. For an Implementation Engineer verifying the bill of materials or configuring the switch ports, it is critical to know that the R4 optics are native 64Gb. If the customer's switch infrastructure is only 16Gb or 32Gb, the ports will auto-negotiate

down, but the shipped default hardware is the 64Gb standard (Option C). Option A (16Gb) and Option B (32Gb) refer to the default standards for the older //M and //X R2/R3 generations respectively.

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