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## Pure Storage Certified FlashArray Implementation Specialist Sample Questions (Q48-Q53):

### NEW QUESTION # 48

An Implementation Engineer is beginning a controller upgrade. Which command should be run to stop Purity?

- A. pureadm stop --secondary
- B. pureadm stop
- C. pureadm stop --controller CTO

**Answer: C**

Explanation:

The command `pureadm stop --controller CT0` specifically stops Purity on the designated controller (CT0), which is the correct step to safely begin a controller upgrade.

#### NEW QUESTION # 49

Which Purity command can be used to validate I/O is balanced across initiators?

- A. `purehost monitor --balance`
- B. `purehost list --balance`
- C. `purehost iobalance --check`

**Answer: C**

Explanation:

The `purehost iobalance --check` command validates whether I/O is balanced evenly across initiators, helping ensure optimal performance and distribution.

#### NEW QUESTION # 50

For FlashArray//XL, which PCIe card slot is reserved for control upgrades?

- A. 0
- B. 1
- C. 2

**Answer: C**

Explanation:

FlashArray//XL models are designed with 9 PCIe slots per controller, providing ample connectivity options for various components. Slot 5 is specifically reserved for non-disruptive upgrade (NDU) procedures, ensuring that controller upgrades can be performed without impacting system availability. This slot allocation is part of the system's design to facilitate seamless upgrades and maintain operational continuity.

#### NEW QUESTION # 51

When executing a FlashArray upgrade from the FA-405 to a FA-X Series, into which slot on CT1 must the 2 Port InfiniBand Card be installed?

- A. 0
- B. 1
- C. 2

**Answer: A**

Explanation:

In the FlashArray//XR2 and //XR3 models, the 2-port InfiniBand card should be installed in PCIe slot 2 on controller CT1. This slot is designated for InfiniBand connectivity, ensuring optimal performance and compatibility during the upgrade process. Installing the card in the correct slot is crucial for maintaining proper system functionality and achieving the desired performance metrics.

#### NEW QUESTION # 52

Which enablement feature will cause Mirrored Write statistics to appear in the Analysis section of the FlashArray Purity 6.x GUI?

- A. `ActiveCluster (AC)`
- B. `ActiveDR`
- C. `FA File`

**Answer: A**

Explanation:

Mirrored Write statistics appear in the Purity GUI's Analysis section when ActiveCluster is enabled, as it tracks synchronous replication activity between clusters.

## NEW QUESTION # 53

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