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NVIDIA AI Infrastructure Sample Questions (Q151-Q156):

NEW QUESTION # 151

You are troubleshooting a performance issue with NVMe-oF traffic being accelerated by a BlueField-2 DPU. You suspect a problem with the RDMA configuration. Which of the following 'perfquery' commands would provide the MOST relevant information to diagnose potential RDMA issues such as packet loss or congestion?

- A. 'perfquery -x' (general link statistics)
- B. 'perfquery' (QOS statistics)
- C. 'perfquery -G' (global counters)
- **D. 'perfquery -P' (port counters including packet loss and congestion)**
- E. 'perfquery -s;' (switch statistics)

Answer: D

Explanation:

'perfquery -P' provides port counters, including critical information about packet loss, congestion, and other RDMA-related metrics at the port level. This is the MOST relevant command for diagnosing performance problems related to RDMA within an NVMe-oF setup. Other options provide less specific or relevant information.

NEW QUESTION # 152

You are running a distributed training job across multiple nodes, using a shared file system for storing training data. You observe that some nodes are consistently slower than others in reading data. Which of the following could be contributing factors to this performance discrepancy? Select all that apply.

- **A. Network congestion between the slower nodes and the storage system.**
- B. Different CPU architectures on the nodes.
- C. Variations in the speed of the local temporary storage (e.g., /tmp) used for intermediate files.
- **D. Uneven data distribution across the storage nodes.**
- **E. Insufficient RAM on the slower nodes for caching data.**

Answer: A,D,E

Explanation:

Network congestion (A) can directly impact the data transfer rate between the slower nodes and the storage. Uneven data distribution (B) means some storage nodes are more heavily loaded, leading to slower response times for nodes accessing data on those overloaded nodes. Insufficient RAM (D) limits the amount of data that can be cached locally, forcing more frequent reads from the slower storage system. CPU architecture (C) primarily affects compute performance, not I/O. The speed of /tmp (E) is relevant if the training job uses local storage extensively for temporary files, but the question focuses on reading training data from the shared file system.

NEW QUESTION # 153

A developer reports that their CUDA application running on a MIG instance is experiencing significantly reduced memory bandwidth compared to running on a full GPU. What are the potential causes for this performance bottleneck? (Select all that apply)

- **A. The application is not optimized to take advantage of the MIG instance's specific memory bandwidth limitations.**
- B. MIG instances inherently provide higher memory bandwidth due to their partitioned nature, so this report must be incorrect.
- **C. The CUDA driver version is not compatible with the MIG configuration, resulting in reduced performance.**
- **D. The application is exceeding the memory capacity of the MIG instance, leading to excessive swapping.**
- **E. The MIG instance has a smaller memory allocation compared to the full GPU, thus limiting the application's memory footprint.**

Answer: A,C,D,E

Explanation:

MIG instances have smaller memory allocations (A) compared to the full GPU, which naturally limits memory footprint. Applications may not be optimized for MIG's bandwidth limitations (B) and might require tuning. Exceeding memory capacity will trigger swapping (C), significantly reducing performance. Incompatible CUDA drivers (D) can lead to performance degradation. MIG

instances don't inherently offer higher bandwidth (E); they divide the overall GPU resources.

NEW QUESTION # 154

You are setting up a multi-GPU AI server for deep learning. You want to ensure optimal inter-GPU communication. Which of the following interconnect technologies would provide the BEST performance?

- A. Infiniband
- **B. NVLink**
- C. Ethernet
- D. PCIe Gen4 x16
- E. PCIe Gen3 x16

Answer: B

Explanation:

NVLink is designed specifically for high-bandwidth, low-latency inter-GPU communication, offering significantly better performance than PCIe or network connections for workloads that benefit from it. InfiniBand is suitable for node to node communication, while NVLink is for GPU to GPU on the same node.

NEW QUESTION # 155

Which of the following are key considerations when choosing between CPU pinning and NUMA (Non-Uniform Memory Access) awareness for a distributed training job on a multi-socket AMD EPYC server with multiple GPUs?

- A. NUMA awareness is generally more important than CPU pinning because it directly impacts memory bandwidth.
- **B. CPU pinning ensures that each process/thread runs on a specific CPU core, reducing context switching overhead. NUMA awareness ensures that the CPU cores and memory used by a process are located within the same NUMA node, minimizing memory access latency.**
- C. CPU pinning is generally more important than NUMA awareness because it directly impacts CPU utilization.
- D. Neither CPU pinning nor NUMA awareness are relevant for GPU-accelerated workloads, as the GPUs handle all the computation.
- **E. Both CPU pinning and NUMA awareness are critical for optimizing performance. They should be used in conjunction to achieve optimal performance.**

Answer: B,E

Explanation:

CPU pinning and NUMA awareness are both important for optimizing performance on multi-socket systems. CPU pinning reduces context switching overhead, while NUMA awareness reduces memory access latency. Using them together provides the best performance. They ensure the CPU and memory being used are on the same node, which reduces latency. While GPUs handle intense computation, CPUs prepare and feed data to the GPUs, and are critical for performance.

NEW QUESTION # 156

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