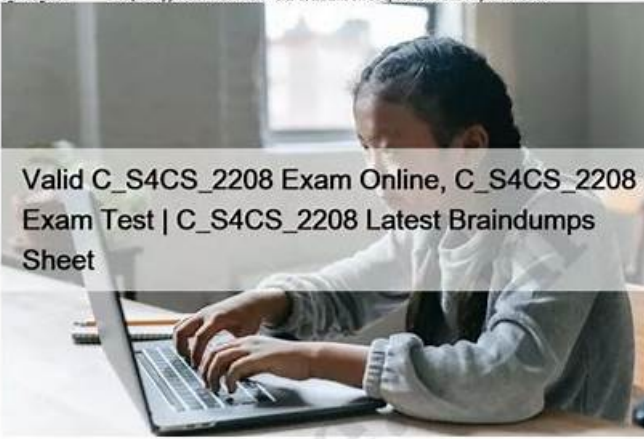


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## NVIDIA-Certified Professional AI Networking Sample Questions (Q52-Q57):

### NEW QUESTION # 52

You're designing a multi-GPU system for AI training using NVIDIA GPUs with NVLink connections.

You need to maximize inter-GPU communication bandwidth. Which feature included in NCCL allows for improved communication between GPUs and NICs?

- **A. PXN**
- B. Graph Search Optimization
- C. Adaptive Routing
- D. SHARP v2

**Answer: A**

Explanation:

The correct answer is PXN (Peer eXchange Network).

From the NVIDIA NCCL Documentation:

"PXN enables communication between GPUs connected via NVLink and NICs by treating the GPUs as a distributed switch. This architecture improves bandwidth utilization by enabling any GPU to communicate with the NIC via the shortest path available, even if it's not directly connected to the NIC." This enhances GPU-to-NIC and NIC-to-GPU transfers, leveraging the NVLink topology. It significantly boosts performance in multi-GPU setups where not every GPU is directly connected to the NIC.

Other options:

- \* Adaptive Routing is a fabric-level feature for dynamic path rerouting.
- \* Graph Search Optimization is used internally for topology modeling in NCCL.
- \* SHARP v2 is a switch-based collective acceleration method, unrelated to PXN.

Reference: NVIDIA NCCL User Guide - PXN Feature Section

### NEW QUESTION # 53

You are optimizing an InfiniBand network for AI workloads that require low-latency and high-throughput data transfers. Which feature of InfiniBand networks minimizes CPU overhead during data transfers?

- A. TCP/IP Offloading
- **B. Direct Memory Access (DMA)**
- C. SHARP
- D. PKey

**Answer: B**

Explanation:

Direct Memory Access (DMA) in InfiniBand networks allows data to be transferred directly between the memory of two devices without involving the CPU. This capability significantly reduces CPU overhead, lowers latency, and increases throughput, making it ideal for AI workloads that demand efficient data transfers.

### NEW QUESTION # 54

In which mode of the BlueField DPU does the ARM system on the DPU control the NIC data path, but allow access to the DPU OS from the host?

- A. Restricted mode
- **B. DPU mode**
- C. Separated Host mode
- D. NIC mode

**Answer: B**

Explanation:

In DPU Mode, the ARM cores on BlueField own the NIC data path, while still allowing the host system to access the DPU OS (via OOB or virtio).

From NVIDIA BlueField Documentation:

"In DPU Mode, the data path is offloaded to the BlueField Arm cores, enabling advanced security and networking functions, while still allowing host access to the BlueField OS." This is different from:

- \* NIC Mode: Data path controlled by host, ARM cores inactive.
- \* Separated Host Mode: Complete isolation; host cannot access DPU OS.
- \* Restricted Mode: Limited host access to DPU OS, but without full offload capabilities.

Reference: NVIDIA BlueField DPU Architecture Guide - Operating Modes Section

### NEW QUESTION # 55

You're troubleshooting a Spectrum-X network and notice that the System Status LED on a switch is blinking for more than 5 minutes. What is the most likely cause of this issue?

- A. The power supply unit is failing
- **B. The Onyx software did not boot properly**
- C. The switch is overheating

**Answer: B**

Explanation:

According to the NVIDIA Spectrum-X Switch Operating System (SX\_OS) Troubleshooting Guide, the System Status LED behavior is a critical indicator of the switch's internal operational state.

From the document:

"The System Status LED will blink green during system initialization. If the LED continues blinking for more than 5 minutes, it indicates that the Onyx OS has failed to load properly. The system may be stuck in the boot process, or the file system may be corrupted." This blinking LED beyond normal initialization time indicates that the system has either encountered a failure during software boot or is unable to transition from bootloader to the OS runtime environment (i.e., Onyx).

Key causes include:

- \* Corrupted or missing system files.
- \* Failed firmware or OS upgrade attempts.
- \* Boot device (e.g., eMMC or SSD) issues or corrupted partitions.

Technically, during power-on:

- \* The switch performs POST (Power-On Self Test).
- \* Then the Onyx OS attempts to load from the boot partition.
- \* If the Onyx OS kernel or root filesystem is invalid, the system halts boot, and the LED remains in a blinking state, as no successful OS load confirmation is triggered.

Remediation Steps (as per NVIDIA guide):

- \* Access the switch through console and monitor boot logs.
- \* Use ONIE recovery or re-flash a stable Onyx OS version.
- \* Check system storage integrity using built-in diagnostics.

Exact Extract Reference:

Source: NVIDIA SX\_OS 3.9.3000 Documentation

Topic: Troubleshooting System Status LED

Extract: "If the LED blinks for more than 5 minutes and the switch is not accessible via CLI, the Onyx software failed to load properly and recovery procedures must be initiated."

### NEW QUESTION # 56

You are optimizing an AI workload that involves multiple GPUs across different nodes in a data center. The application requires both high-bandwidth GPU-to-GPU communication within nodes and efficient communication between nodes.

Which combination of NVIDIA technologies would best support this multi-node, multi-GPU AI workload?

- A. InfiniBand for both intra-node and inter-node GPU communication.
- B. NVLink for both intra-node and inter-node GPU communication.
- **C. NVLink for intra-node GPU communication and InfiniBand for inter-node communication.**
- D. PCIe for intra-node GPU communication and RoCE for inter-node communication.

**Answer: C**

Explanation:

For optimal performance in multi-node, multi-GPU AI workloads:

\* NVLink provides high-speed, low-latency communication between GPUs within the same node.

\* InfiniBand offers efficient, scalable communication between nodes in a data center. Combining these technologies ensures both intra-node and inter-node communication needs are effectively met.

Reference: NVIDIA NVLink & NVSwitch: Fastest HPC Data Center Platform

## NEW QUESTION # 57

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