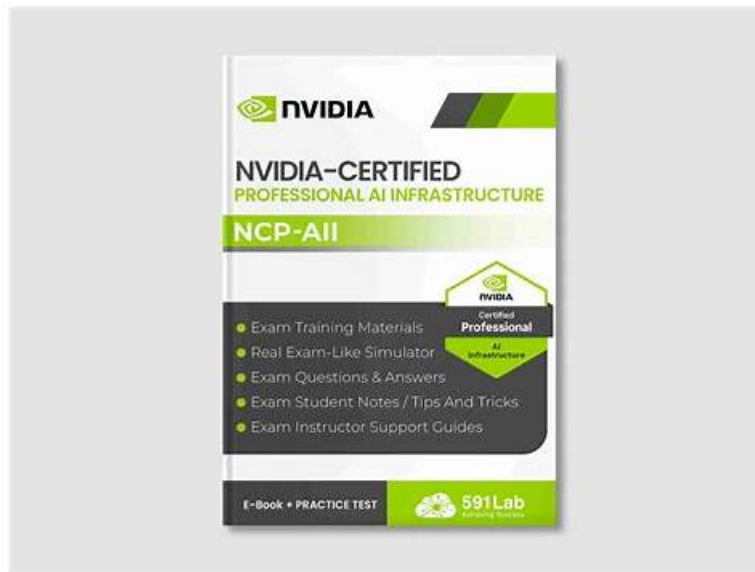


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NVIDIA AI Infrastructure Sample Questions (Q123-Q128):

NEW QUESTION # 123

You are designing an AI infrastructure using NVIDIA HGX A100 servers. These servers support both PCIe Gen4 and NVLink for GPU interconnects. Which statement is MOST accurate concerning the choice between PCIe Gen4 and NVLink for inter-GPU communication within a single HGX A100 server for deep learning training?

- A. NVLink provides a direct, high-bandwidth, low-latency connection between GPUs, which is generally superior to PCIe Gen4 for deep learning training involving frequent inter-GPU communication.
- B. PCIe Gen4 offers higher bandwidth and lower latency compared to NVLink, making it the preferred choice for all deep learning workloads.

- C. PCIe Gen4 is more cost-effective and power-efficient than NVLink, making it the optimal choice for smaller AI training datasets.
- D. NVLink and PCIe Gen4 offer identical performance for inter-GPU communication; the choice is arbitrary.
- E. The choice between PCIe Gen4 and NVLink depends solely on the type of deep learning framework being used; TensorFlow requires PCIe Gen4, while PyTorch benefits from NVLink.

Answer: A

Explanation:

NVLink is specifically designed for high-bandwidth, low-latency communication between GPUs, making it superior to PCIe Gen4 for deep learning training where GPUs frequently exchange data. NVLink allows GPUs to share memory directly.

NEW QUESTION # 124

You are troubleshooting a performance issue on an Intel Xeon server with NVIDIA A100 GPUs. Your application involves frequent data transfers between CPU memory and GPU memory. You suspect that the PCIe bus is a bottleneck. How can you verify and mitigate this bottleneck?

- A. Check the CPU utilization. If it's low, the PCIe bus is likely the bottleneck. Mitigate by increasing the number of CPU cores assigned to the data transfer tasks.
- B. Monitor the GPU temperature. If it's high, the PCIe bus is likely overheating. Mitigate by improving the server's cooling.
- C. Use 'nvprof' to profile the application and identify the exact lines of code that are causing the high PCIe traffic. Optimize those sections of code to reduce data transfers.
- D. Use 'nvidia-smi' to monitor the PCIe bandwidth utilization of the GPUs. If it's consistently high (near the theoretical limit), the PCIe bus is likely a bottleneck. Mitigate by reducing the frequency of CPU-GPU data transfers, using pinned (page-locked) memory, and ensuring that the GPUs are connected to PCIe slots with sufficient bandwidth.
- E. Examine the system logs for PCIe errors. If there are many errors, the PCIe bus is likely unstable. Mitigate by reseating the GPUs and checking the power supply.

Answer: C,D

Explanation:

'nvidia-smi' allows monitoring PCIe bandwidth utilization, directly indicating a bottleneck. Pinned memory helps with efficient DMA transfers. Reducing transfer frequency and code optimization using 'nvprof' are valid mitigation strategies. Low CPU utilization doesn't necessarily indicate PCIe bottleneck. PCIe errors indicate instability, not necessarily high utilization. GPU temperature is related to cooling, not directly the PCIe bus being a bottleneck.

NEW QUESTION # 125

You need to verify the NVLink connectivity between GPUs in a DGX server. Which command-line utility is the MOST reliable and provides detailed NVLink status?

- A. nvidia-smi
- B. lspci
- C. gpustat
- D. nvlink_info (Hypothetical command)
- E. dcgm-diag -t 1004

Answer: E

Explanation:

'dcgm-diag -t 1004' is the correct command. 'nvidia-smi' provides basic GPU information, but 'dcgm-diag -t 1004' (part of the Data Center GPU Manager) provides specific diagnostic tests for NVLink connectivity. 'lspci' lists PCIe devices, not specifically NVLink. 'gpustat' is a monitoring tool. 'nvlink_info' is hypothetical.

NEW QUESTION # 126

You are deploying a BlueField-3 DPU within a secure environment. You are required to enable secure boot to prevent unauthorized firmware from being loaded. Which steps are typically involved in enabling secure boot on a BlueField-3 DPU, starting from a factory-default state? (Select TWO)

- A. Configure the server's BIOS to enable UEFI boot mode.
- B. Install the latest version of the Mellanox OFED drivers on the host server.
- C. Generate cryptographic keys and enroll them in the DPIJ's IJEFI firmware using tools provided by NVIDIA or Mellanox.
- D. Flash the BlueField-3 DPIJ with a secure boot-enabled OS image that is signed with the enrolled keys.
- E. Update the DPU's BMC firmware to the latest version.

Answer: C,D

Explanation:

The crucial steps are generating and enrolling cryptographic keys in the DPIJ's UEFI firmware, which establishes the chain of trust, and flashing the DPIJ with a secure boot-enabled OS image signed with those keys. Without these, secure boot won't function correctly. While updating the BMC firmware (E) is generally a good practice, and enabling IJEFI boot mode in the server's BIOS (C) is necessary for UEFI in general, they are not specific steps for enabling secure boot on the DPIJ itself. Installing host drivers (A) is also irrelevant to secure boot.

NEW QUESTION # 127

You have a large dataset stored on a BeeGFS file system. The training job is single node and uses data augmentation to generate more data on the fly. The data augmentation process is CPU-bound, but you notice that the GPU is underutilized due to the training data not being fed to the GPU fast enough. How can you reduce the load on the CPU and improve the overall training throughput?

- A. Enable data compression on the BeeGFS file system to reduce the amount of data being transferred over the network.
- B. Implement asynchronous I/O in the data loading pipeline using a library like NVIDIA DALI to offload data processing tasks from the CPU to the GPU.
- C. Increase the number of BeeGFS metadata servers (MDSs) to improve metadata performance.
- D. Move the training data to a local NVMe drive on the training node.
- E. Decrease the batch size of the training job to reduce the amount of data being processed at each iteration.

Answer: B

Explanation:

Using NVIDIA DALI (option B) allows you to offload data augmentation and preprocessing tasks from the CPU to the GPU, freeing up CPU resources for other tasks and enabling faster data loading. Moving to a local NVMe drive (A) bypasses BeeGFS but doesn't address the CPU bottleneck. Increasing MDSs (C) improves metadata performance but doesn't directly help with the CPU-bound data augmentation. Decreasing the batch size (D) reduces the workload but doesn't solve the underlying CPU bottleneck. Data compression (E) can increase CPU load due to the decompression process.

NEW QUESTION # 128

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