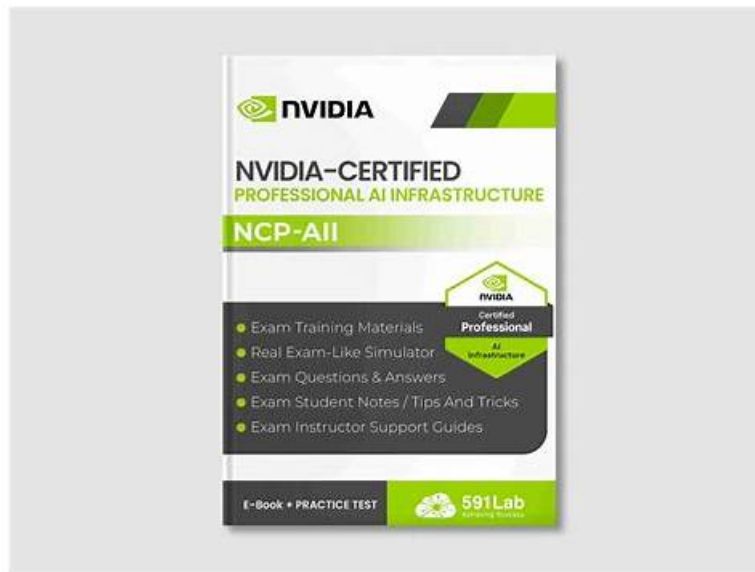


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NVIDIA AI Infrastructure Sample Questions (Q197-Q202):

NEW QUESTION # 197

You are deploying a new AI cluster using RoCEv2 over a lossless Ethernet fabric. Which of the following QOS (Quality of Service) mechanisms is critical for ensuring reliable RDMA communication?

- A. DSCP (Differentiated Services Code Point) marking
- B. Rate Limiting
- C. PFC (Priority Flow control)
- D. ACL (Access Control List)

- E. ECN (Explicit Congestion Notification)

Answer: C

Explanation:

PFC (Priority Flow Control) is essential for RoCEv2 over Ethernet. It prevents packet loss due to congestion by pausing traffic on a specific link, ensuring reliable RDMA communication. DSCP is used for traffic prioritization but doesn't prevent loss. ECN signals congestion but relies on endpoints to react. ACLs are for security. Rate Limiting will cause unnecessary Packet Loss.

NEW QUESTION # 198

You are setting up a multi-node A1 cluster with NVIDIA GPUs and InfiniBand for inter-node communication. You need to ensure the InfiniBand network is functioning optimally for GPU-accelerated workloads. What steps would you take to validate the InfiniBand installation and performance?

- A. Verify the InfiniBand drivers are installed and then run a standard TCP benchmark between the nodes.
- B. Use 'nvidia-smi' to monitor InfiniBand traffic, and rely on CUDA-aware MPI for communication validation.
- C. Run 'ibstat' to check InfiniBand interface status, use 'ibping' and 'ibperf' to test latency and bandwidth, and verify correct NCCL configuration (e.g., during a distributed training run).
- D. Run 'ibstat' to check InfiniBand interface status, use 'ping' to test connectivity, and rely on NCCL's internal checks during training.
- E. Configure a static IP address on the InfiniBand interfaces, and rely on the operating system's network diagnostics.

Answer: C

Explanation:

'ibstat' verifies interface status. 'ibping' and 'ibperf' are InfiniBand-specific tools for latency and bandwidth testing. NCCL (NVIDIA Collective Communications Library) is critical for distributed training, and provides valuable diagnostic information. The other options are either incomplete or rely on tools not specific to InfiniBand.

NEW QUESTION # 199

You are installing eight NVIDIA A100 GPUs in a server designed for maximum performance. The server supports NVLink. Which of the following actions will BEST improve the inter-GPU communication bandwidth?

- A. Only installing four of the eight GPUs to reduce the total number of connections needed.
- B. Installing the GPUs in PCIe Gen3 slots instead of PCIe Gen4 slots.
- C. Using a standard PCIe riser card for all GPUs.
- D. Disabling NVLink in the BIOS/UEFI settings.
- E. Ensuring the GPUs are placed in slots that support NVLink bridges and that the bridges are properly installed.

Answer: E

Explanation:

NVLink provides significantly higher bandwidth than PCIe for inter-GPU communication. Installing the GPUs in slots that support NVLink and properly installing the NVLink bridges will enable this faster communication. PCIe Gen4 is better than Gen3. Riser cards don't improve communication bandwidth. Disabling NVLink negates its benefits. Removing GPUs reduces overall performance.

NEW QUESTION # 200

Consider a scenario where you are running a CUDA application on an NVIDIA GPU. The application compiles successfully but crashes during runtime with a *CUDA_ERROR_ILLEGAL_ADDRESS* error. You've carefully reviewed your code and can't find any obvious out-of-bounds memory accesses. What advanced debugging techniques could help you pinpoint the source of this error?

- A. Utilize NVIDIA Nsight Systems to profile the application and identify memory allocation patterns.
- B. Reduce the block size used in CUDA kernels to decrease the likelihood of shared memory conflicts.
- C. Enable ECC (Error Correction Code) memory on the GPU to detect and correct memory errors.
- D. Use 'cuda-memcheck' to detect memory access errors at runtime.
- E. Employ the CUDA Debugger (cuda-gdb) to step through the code and inspect variable values and memory contents.

Answer: A,D,E

Explanation:

'cuda-memcheck' (A) is specifically designed to detect memory access errors. 'cuda-gdb' (B) allows for detailed code inspection. NVIDIA Nsight Systems (C) provides profiling information that can help identify memory allocation issues that lead to the error. Enabling ECC (D) might mask the symptom but doesn't fix the underlying code error, also ECC is enabled by default, so that's a red herring. Reducing block size (E) is only relevant if shared memory is involved and potentially overflowing.

NEW QUESTION # 201

After replacing a faulty NVIDIA GPU, the system boots, and 'nvidia-smi' detects the new card. However, when you run a CUDA program, it fails with the error "no CUDA-capable device is detected". You've confirmed the correct drivers are installed and the GPU is properly seated. What's the most probable cause of this issue?

- A. The new GPU is incompatible with the existing system BIOS.
- **B. The GPJ is not properly initialized by the system due to a missing or incorrect ACPI configuration.**
- C. The 'LD LIBRARY PATH' environment variable is not set correctly.
- D. The CUDA toolkit is not properly configured to use the new GPU.
- E. The user running the CUDA program does not have the necessary permissions to access the GPU.

Answer: B

Explanation:

The error "no CUDA-capable device is detected", even when 'nvidia-smi' sees the GPJ, points to a lower-level system issue that prevents CUDA from accessing the card. In such scenarios, ACPI (Advanced Configuration and Power Interface) misconfiguration is frequently the culprit. ACPI handles device initialization and power management. If ACPI doesn't properly configure the new GPU, CUDA programs won't be able to access it. Checking and correcting ACPI configuration would be the first line of action, which includes ensuring proper settings in the system BIOS/IJEFI related to PCI devices, especially those related to GPU/accelerators. LD LIBRARY PATH would affect runtime linking of CUDA libraries, but not the base device detection. User permissions are less likely to be the cause since 'nvidia-smr' works.

NEW QUESTION # 202

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