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NVIDIA AI Infrastructure Sample Questions (Q141-Q146):

NEW OUESTION # 141

Your A1 inference server utilizes Triton Inference Server and experiences intermittent latency spikes. Profiling reveals that the GPU is frequently stalling due to memory allocation issues. Which strategy or tool would be least effective in mitigating these memory allocation stalls?

- A. Reducing the model's memory footprint by using quantization or pruning techniques.
- B. Increasing the GPU's TCC (Tesla Compute Cluster) mode priority.
- C. Enabling CUDA graph capture to reduce kernel launch overhead.
- D. Using CIJDA memory pools to pre-allocate memory and reduce allocation overhead during inference requests.
- E. Optimize the model using TensorRT.

Answer: B

Explanation:

CUDA memory pools directly address memory allocation overhead. CUDA graph capture reduces kernel launch overhead, which can indirectly reduce memory pressure. Model quantization/pruning reduces the overall memory footprint. Optimizing using TensorRT reduces memory footprint. Increasing TCC priority primarily affects preemption behavior and doesn't directly address memory allocation issues. Therefore it will have less impact than others.

NEW QUESTION #142

An NVIDIA DGX server with 8 GPUs is experiencing performance issues during a distributed deep learning training run. You suspect a problem with the GPU interconnects. You have already confirmed that NVLink is active. What is the most thorough approach to diagnose potential bandwidth or latency bottlenecks in the GPU-to-GPIJ communication paths?

- A. Run NCCL all-reduce benchmarks (e.g., using the NCCL tests) to measure the actual communication bandwidth between all pairs of GPUs. Compare the results to expected theoretical peak bandwidth.
- B. Use 'nvidia-smi topo -m' to visualize the GPU topology and check the reported link speeds. Any links with significantly lower speeds are suspect.
- C. All of the above
- D. Monitor GPU utilization with 'nvidia-smi' during the training run. Uneven utilization across GPUs indicates a potential communication bottleneck.
- E. Examine the output of 'dmesg' for any NVLink-related error messages or warnings.

Answer: C

Explanation:

Explanation:Diagnosing GPU interconnect bottlenecks requires a multi-faceted approach. Visualizing the topology with 'nvidia-smi topo -m', running NCCL benchmarks, monitoring GPU utilization for imbalances, and checking system logs for errors are all necessary for identifying and resolving communication issues. Ignoring any of these steps could lead to incomplete diagnosis and suboptimal performance.

NEW QUESTION # 143

Which command is used to verify the installation and configuration of the NGC CLI after initial setup?

- A. 'ngc config validate'
- B. 'ngc -version'
- C. 'ngc registry model list'
- D. 'ngc setup verify'
- E. 'ngc system statue'

Answer: B,C

Explanation:

'ngc -version' confirms the CLI is installed and shows its version. 'ngc registry model list' verifies that the CLI can authenticate with NGC and access the registry.

NEW QUESTION # 144

Consider a distributed training job running across multiple nodes, each with local NVMe storage. You want to minimize network traffic and maximize I/O performance. Which data loading strategy would be MOST effective?

- A. Centralized data loading from a single NFS server
- B. Using rsync to copy data between nodes before each epoch
- C. Using object storage (e.g., S3) as the primary data source and loading data on demand
- D. Loading the entire dataset into the memory of a single node and then distributing it to the other nodes
- E. Distributing the dataset across the local NVMe drives of each node and using a distributed data loader

Answer: E

Explanation:

Distributing the dataset across the local NVMe drives and using a distributed data loader allows each node to read data directly from its local storage, minimizing network traffic and maximizing I/O performance. Centralized data loading from NFS will create a bottleneck. Loading into a single node's memory is impractical for large datasets. Object storage can introduce latency. Rsync is inefficient for repeated data loading.

NEW QUESTION # 145

You are configuring a server with multiple GPUs for CUDA-aware MPI. Which environment variable is critical for ensuring proper GPU affinity, so that each MPI process uses the correct GPU?

- A. MPI GPU SUPPORT
- B. CUDA VISIBLE DEVICES
- C. LD LIBRARY PATH
- D. CUDA LAUNCH BLOCKING-I
- E. CUDA DEVICE ORDER

Answer: B

Explanation:

'CUDA VISIBLE DEVICES' is essential for GPU affinity. It allows you to specify which GPUs are visible to a particular process. Without it, all processes might try to use the same GPU, leading to performance bottlenecks. controls the order in which GPUs are enumerated. specifies the path to shared libraries. is hypothetical, forces synchronous CUDA calls.

NEW QUESTION # 146

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