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NVIDIA AI Infrastructure Sample Questions (Q119-Q124):

NEW QUESTION #119

You are configuring network fabric ports for NVIDIA GPUs in a server. The GPUs are connected to the network via PCIe. What is the primary factor that determines the maximum achievable bandwidth between the GPUs and the network?

- A. The amount of system RAM.
- B. The PCIe generation and number of lanes connecting the GPUs to the network adapter (e.g., PCIe 4.0 x16).
- C. The color of the Ethernet cables.
- D. The speed of the system's hard drives or SSDs.
- E. The clock speed of the CPU.

Answer: B

Explanation:

The PCIe generation (e.g., PCIe 4.0, PCIe 5.0) and the number of lanes (e.g., x8, x16) directly determine the maximum theoretical bandwidth available between the GPUs and the network adapter. Higher PCIe generations and more lanes provide greater bandwidth. For example, PCIe 4.0 x16 offers significantly more bandwidth than PCIe 3.0 x8. All other options are either irrelevant or have a negligible impact on this particular bottleneck.

NEW QUESTION #120

In a large-scale InfiniBand fabric, you need to implement a mechanism to prioritize traffic for a specific application that requires low latency and high bandwidth. You want to leverage Quality of Service (QOS) to achieve this. Which of the following steps are essential to properly configure QOS in this scenario? (Select THREE)

- A. Mark the application's traffic with appropriate DiffServ Code Point (DSCP) values.
- B. Disable Adaptive Routing (AR) to ensure that the application's traffic always takes the shortest path.
- C. Configure VLAN tagging on the application's traffic to isolate it from other traffic.
- D. Map the application's traffic to a specific traffic class with appropriate priority settings within the InfiniBand switches.
- E. Configure Weighted Fair Queueing (WFQ) or Strict Priority Queueing on the egress ports of the InfiniBand switches to prioritize the application's traffic class.

Answer: A,D,E

Explanation:

Effective QOS requires traffic classification (DSCP marking), mapping to appropriate traffic classes with priority settings, and configuring queueing mechanisms (WFQ/Strict Priority Queueing) on egress ports to enforce the priority. VLAN tagging is useful for network segmentation but not directly for QOS. Disabling AR might reduce path diversity, but could also lead to congestion if the shortest path is already heavily utilized.

NEW QUESTION # 121

You are deploying a new A1 cluster using RoCEv2 over a lossless Ethernet fabric. Which of the following QOS (Quality of Service) mechanisms is critical for ensuring reliable RDMA communication?

- A. ECN (Explicit Congestion Notification)
- B. ACL (Access Control List)
- C. PFC (Priority Flow control)
- D. Rate Limiting
- E. DSCP (Differentiated Services Code Point) marking

Answer: C

Explanation:

PFC (Priority Flow Control) is essential for RoCEv2 over Ethernet. It prevents packet loss due to congestion by pausing traffic on a specific link, ensuring reliable RDMA communication. DSCP is used for traffic prioritization but doesn't prevent loss. ECN signals congestion but relies on endpoints to react. ACLs are for security. Rate Limiting will cause unnecessary Packet Loss.

NEW QUESTION # 122

While running a large A1 training job, you observe the following output from 'nvidia-smi':

GPUO: P2

GPU 1: P2

GPU 2: P2

GPU 3: P2

What does the 'P2' state indicate, and what steps should you take to investigate this further in the context of validating optimal hardware operation?

- A. P2 indicates the GPUs are in a high-performance state; no further investigation is needed.
- B. P2 indicates the GPUs are in a low-power idle state; investigate if the driver is correctly configured and the workload is properly utilizing the GPUs.
- C. P2 indicates a critical error state; immediately halt the training job and check the system logs for hardware failures.
- D. P2 indicates the GPU is running at maximum clock speed. Check for thermal throttling.

Answer: B

Explanation:

P2 typically indicates a power-saving state where the GPU is operating at a reduced clock speed. It's crucial to investigate whether the workload is demanding sufficient resources from the GPUs, and whether power limits or other configuration settings are preventing the GPUs from reaching their maximum performance state. Review 'nvidia-smi -q' output for power usage and clock

speeds to verify proper operation. Other power states (PO, PI) represent varying levels of performance.

NEW QUESTION #123

You've installed the NGC CLI, but when you run 'ngc registry model list' you get an error indicating authentication failure. You're sure your API key is correct. What could be the cause, and how would you diagnose this?

- A. The NGC CLI version is outdated. Upgrade to the latest version using 'pip install -upgrade nvidia-cli'.
- B. The host machine's clock is not synchronized, causing authentication issues. Synchronize the clock using 'ntpd' or 'chronyd'.
- C. Your account lacks the necessary permissions to access the NGC registry. Contact your NVIDIA administrator.
- D. Your organization might be behind a proxy that is blocking the NGC CLI from accessing the internet. Configure the proxy settings for the NGC CLI.
- E. The environment variables 'NGC API_KEY or 'NGC CLI_API_KEY are set incorrectly or not set at all. Verify and set them correctly.

Answer: B,C,D

Explanation:

Authentication failures can be caused by proxy issues (C), insufficient account permissions (D), or clock synchronization problems (E). While an outdated CLI version (A) could potentially cause issues, it's less likely to manifest as an authentication failure. Environment variables (B) are generally not the primary source of error when using 'ngc config set' to configure authentication.

NEW QUESTION #124

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