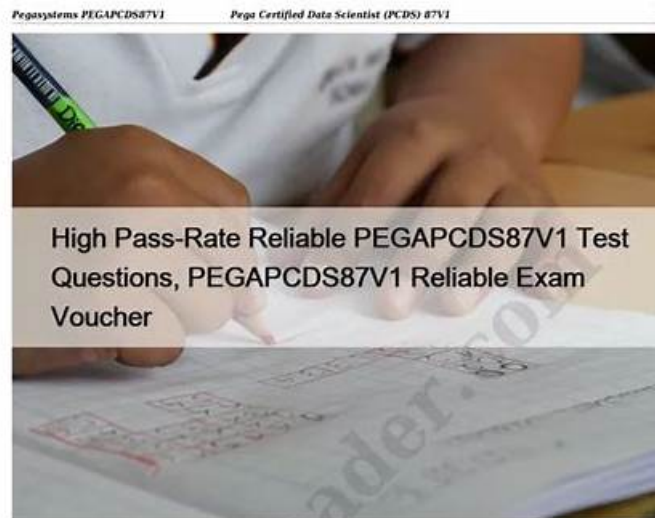


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NVIDIA AI Infrastructure Sample Questions (Q216-Q221):

NEW QUESTION # 216

Which of the following are crucial considerations when validating the hardware operation of an NVIDIA-Certified Professional AI Infrastructure server before deploying a production AI workload? (Select all that apply)

- A. Checking if the server's case fans are spinning.
- B. Confirming the correct installation and functionality of all network interfaces (e.g., Ethernet, InfiniBand) relevant to the workload.
- C. Ensuring the BIOS and BMC firmware are up to date.
- D. Only testing the GPUs with a synthetic benchmark like 'linpack'.
- E. Verifying that the server's power supply units (PSUs) are operating within their specified voltage and current ranges under full load.

Answer: B,C,E

Explanation:

Validating hardware operation involves checking BIOS/BMC firmware for compatibility and security updates, ensuring PSUs are within operating parameters, and verifying network interface functionality. Synthetic benchmarks alone aren't sufficient; the fans spinning is a basic check, but doesn't validate actual cooling performance under load. Complete hardware validation involves a broad range of checks to prevent later issues.

NEW QUESTION # 217

You are configuring a server with multiple GPUs for CUDA-aware MPI. Which environment variable is critical for ensuring proper GPU affinity, so that each MPI process uses the correct GPU?

- A. CUDA LAUNCH BLOCKING-I
- B. MPI GPU SUPPORT
- C. LD LIBRARY PATH
- D. CUDA DEVICE ORDER
- E. CUDA VISIBLE DEVICES

Answer: E

Explanation:

'CUDA VISIBLE DEVICES' is essential for GPU affinity. It allows you to specify which GPUs are visible to a particular process. Without it, all processes might try to use the same GPU, leading to performance bottlenecks. controls the order in which GPUs are enumerated. specifies the path to shared libraries. is hypothetical. forces synchronous CUDA calls.

NEW QUESTION # 218

You are configuring a network bridge on a Linux host that will connect multiple physical network interfaces to a virtual machine. You need to ensure that the virtual machine receives an IP address via DHCP. Which of the following is the correct command sequence to create the bridge interface 'br0', add physical interfaces 'eth0' and 'eth1' to it, and bring up the bridge interface? Assume the required packages are installed. Consider using 'ip' command.

- A.

```
ip link add br0 type bridgeip link set eth0 master br0ip link set eth1 master br0ip link set dev eth0 upip link set dev eth1 upip addr add 192.168.1.1/24 dev br0ip link set dev br0 up
```
- B.

```
ifconfig br0 createifconfig br0 addm eth0ifconfig br0 addm eth1ifconfig br0 updhclient br0
```
- C.

```
ip link add br0 type bridgeip link set eth0 master br0ip link set eth1 master br0ip link set dev br0 updhclient br0
```
- D.

```
brctl addbr br0brctl addif br0 eth0brctl addif br0 eth1ifconfig br0 updhclient br0
```
- E.

```
ip link add br0 type bridge ip link set eth0 master br0 ip link set eth1 master br0 ip link set dev eth0 up ip link set dev eth1 up ip link set dev br0 up dhclient br0
```

Answer: E

Explanation:

Option D is the correct sequence using the 'ip' command. First, create the bridge 'br0'. Then, add the physical interfaces 'eth0' and 'eth1' as slaves to the bridge. Next, bring up the physical interfaces. After that, bring up the bridge interface. Finally, use 'dhclient br0' to obtain an IP address for the bridge via DHCP. Option C is the old way, using 'brctl' and 'ifconfig', which are deprecated. The others lack the crucial step of bringing up the bridge after attaching the physical interfaces and before running 'dhclient'.

NEW QUESTION # 219

While running a large AI training job, you observe the following output from 'nvidia-smi':

GPU 0: P2

GPU 1: P2

GPU 2: P2

GPU 3: P2

What does the 'P2' state indicate, and what steps should you take to investigate this further in the context of validating optimal hardware operation?

- A. P2 indicates the GPU is running at maximum clock speed. Check for thermal throttling.
- **B. P2 indicates the GPUs are in a low-power idle state; investigate if the driver is correctly configured and the workload is properly utilizing the GPUs.**
- C. P2 indicates a critical error state; immediately halt the training job and check the system logs for hardware failures.
- D. P2 indicates the GPUs are in a high-performance state; no further investigation is needed.

Answer: B

Explanation:

P2 typically indicates a power-saving state where the GPU is operating at a reduced clock speed. It's crucial to investigate whether the workload is demanding sufficient resources from the GPUs, and whether power limits or other configuration settings are preventing the GPUs from reaching their maximum performance state. Review 'nvidia-smi -q' output for power usage and clock speeds to verify proper operation. Other power states (P0, P1) represent varying levels of performance.

NEW QUESTION # 220

Which of the following are valid methods for verifying the health and connectivity of InfiniBand links in an NCP-AII environment? (Select TWO)

- A. Checking the system logs ('/var/log/messages' or equivalent) for any InfiniBand-related error messages.
- B. Using 'netstat' to check TCP connections.
- **C. Using 'ibstat' to check the link state, physical state, and other relevant parameters of InfiniBand ports.**
- D. Using 'ping' to test basic IP connectivity over the InfiniBand interface.
- **E. Using 'sminfo' to query the Subnet Manager for network topology and status information.**

Answer: C,E

Explanation:

'ibstat' is a command-line utility specifically designed for checking the status of InfiniBand ports. 'sminfo' allows you to communicate with the Subnet Manager and retrieve network topology and status. While 'ping' can verify IP connectivity over InfiniBand, it doesn't directly assess the health of the InfiniBand link itself. Checking system logs is a useful supplementary task but isn't the primary method.

NEW QUESTION # 221

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